

FIG. 1 PRIOR ART

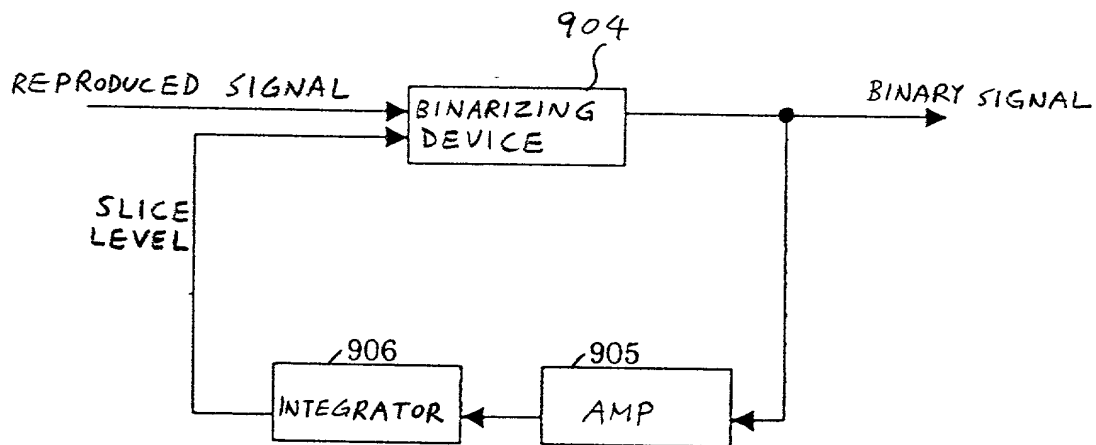


FIG. 2

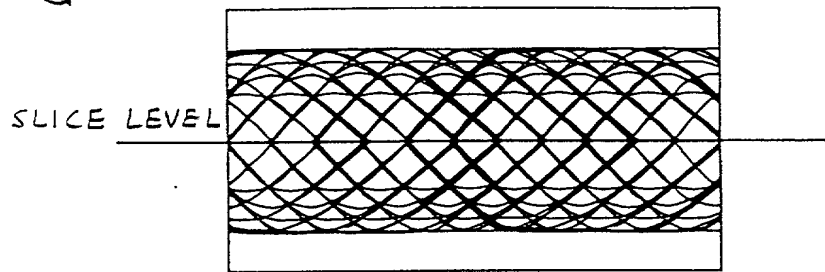


FIG. 3

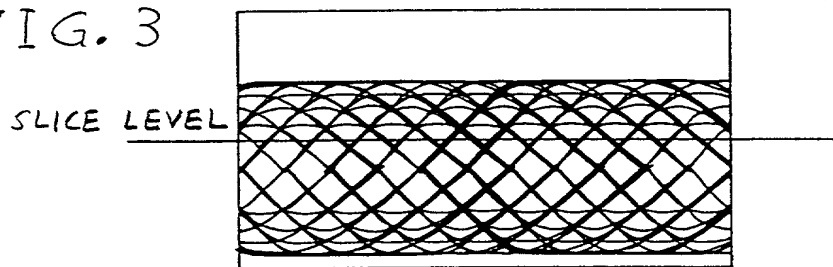


FIG. 4

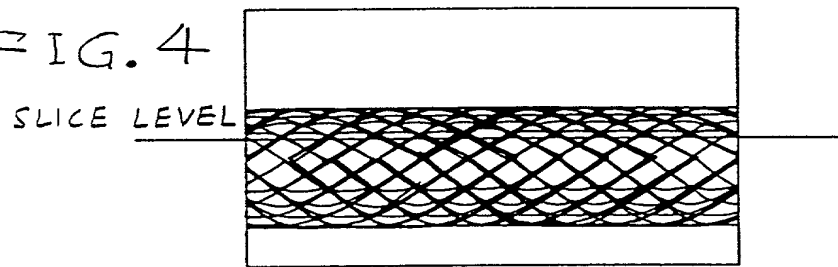
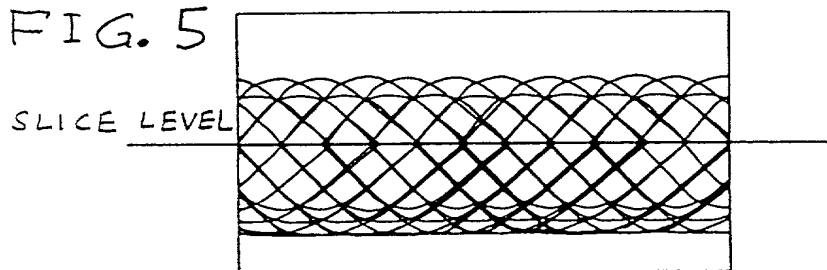


FIG. 5



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FIG. 6

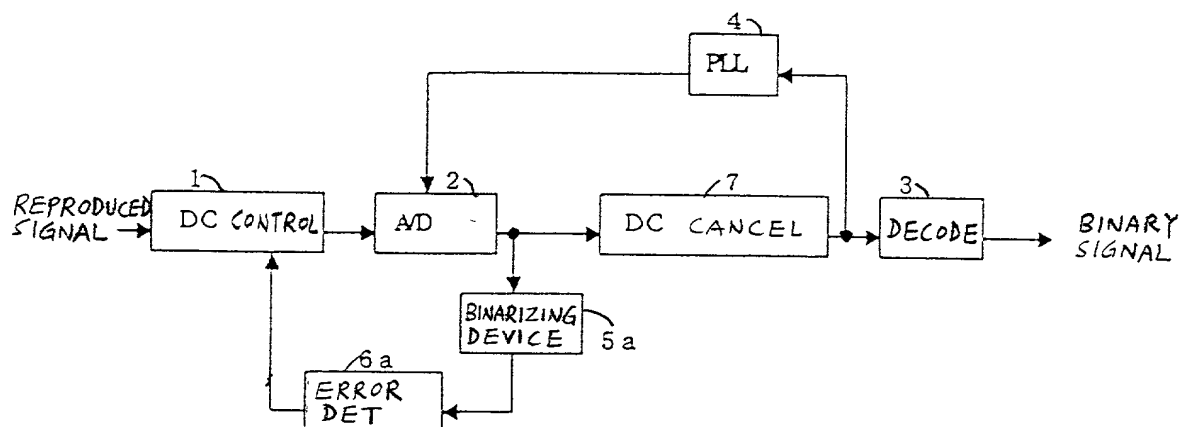


FIG. 7

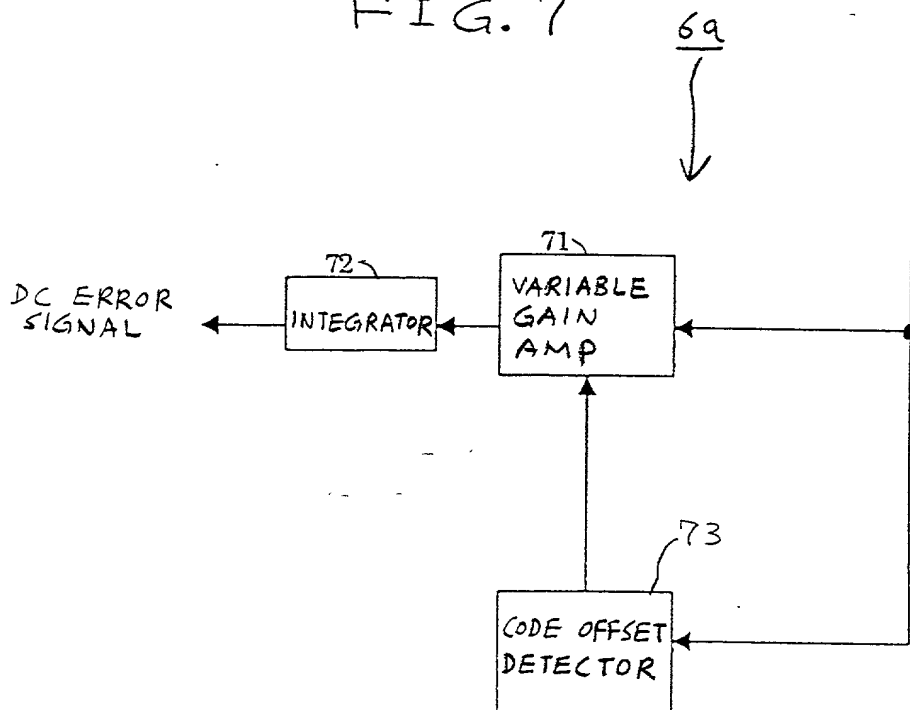


FIG. 8

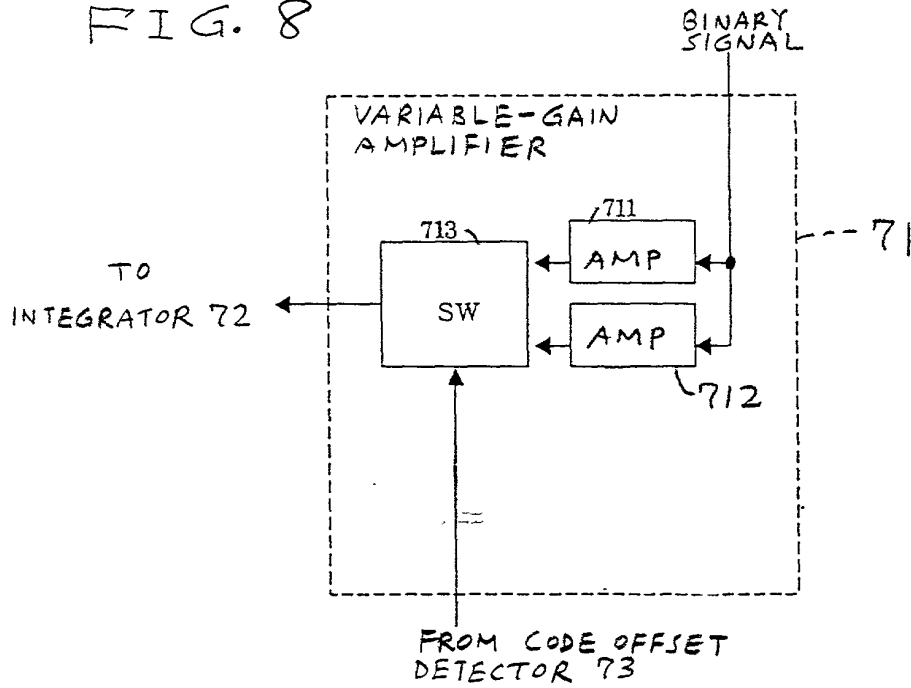
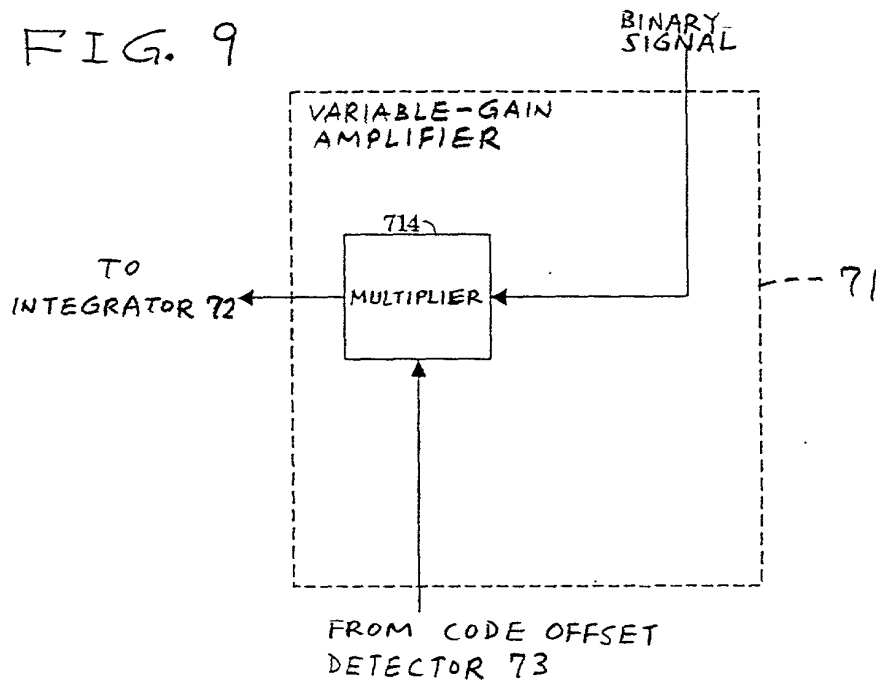


FIG. 9



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73 FIG. 10

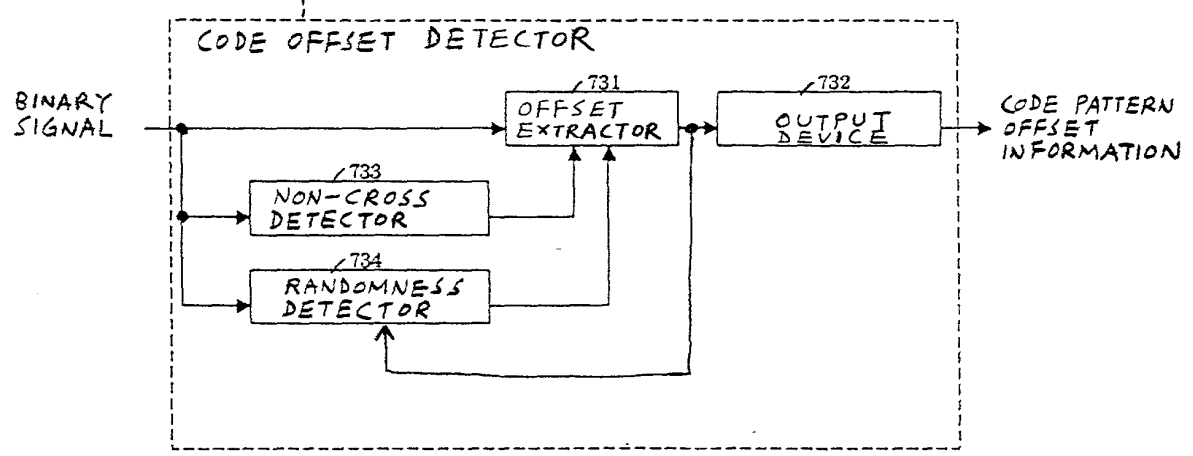


FIG. 11

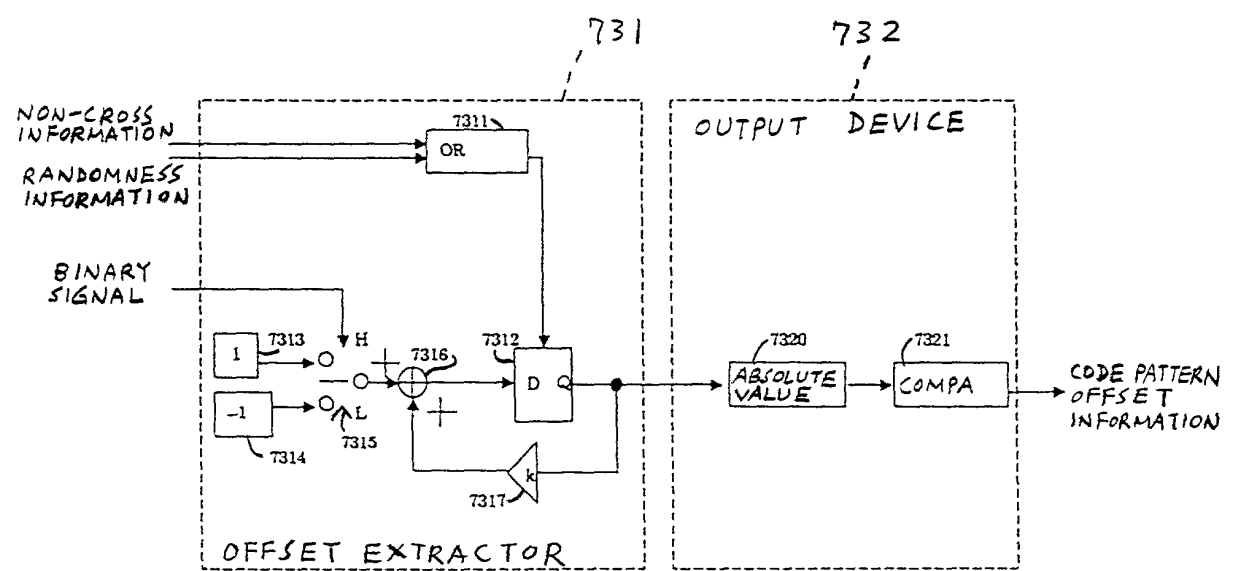


FIG. 12

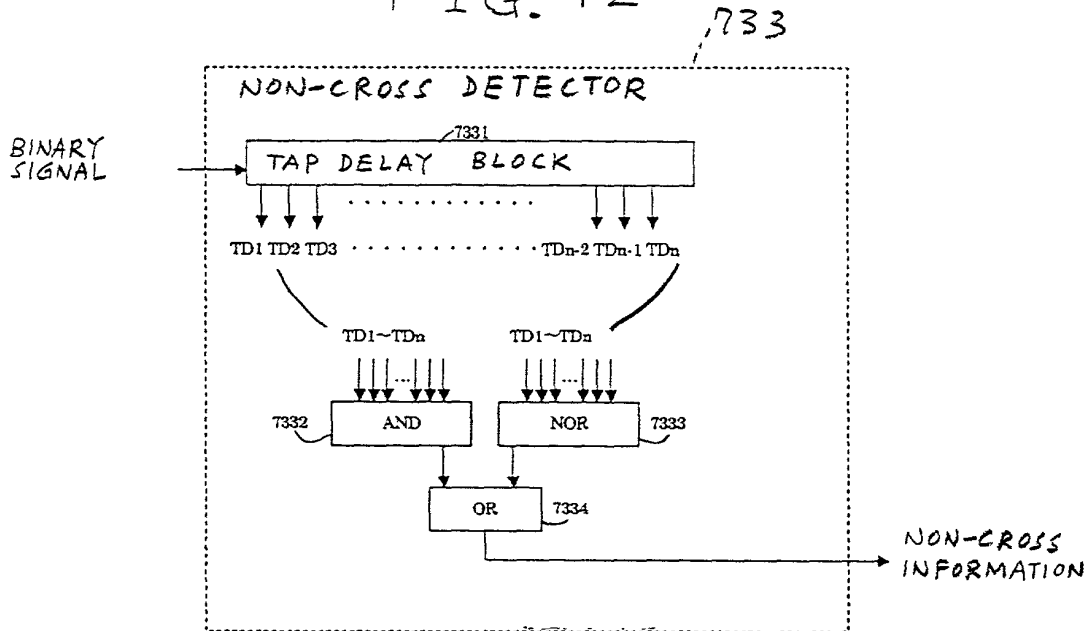


FIG. 13

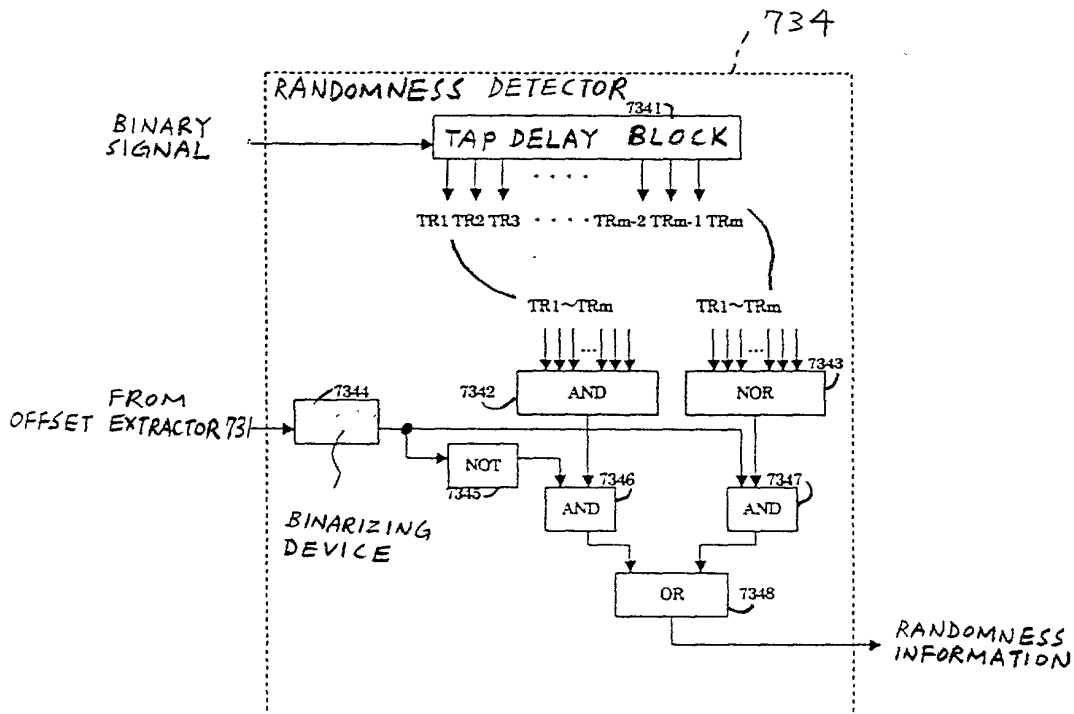


FIG. 14

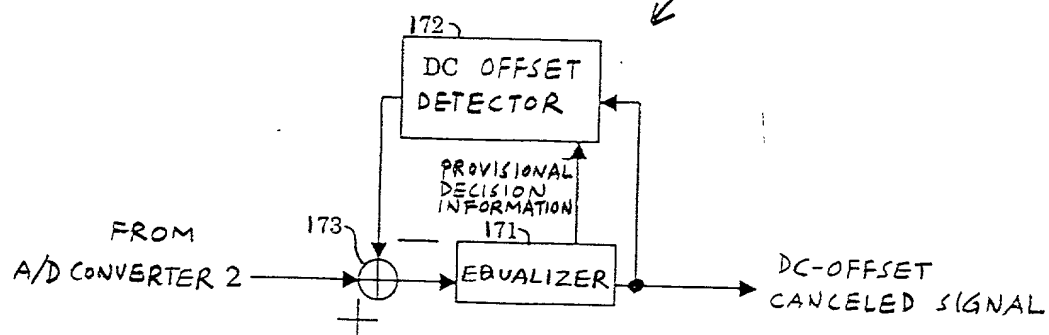


FIG. 15

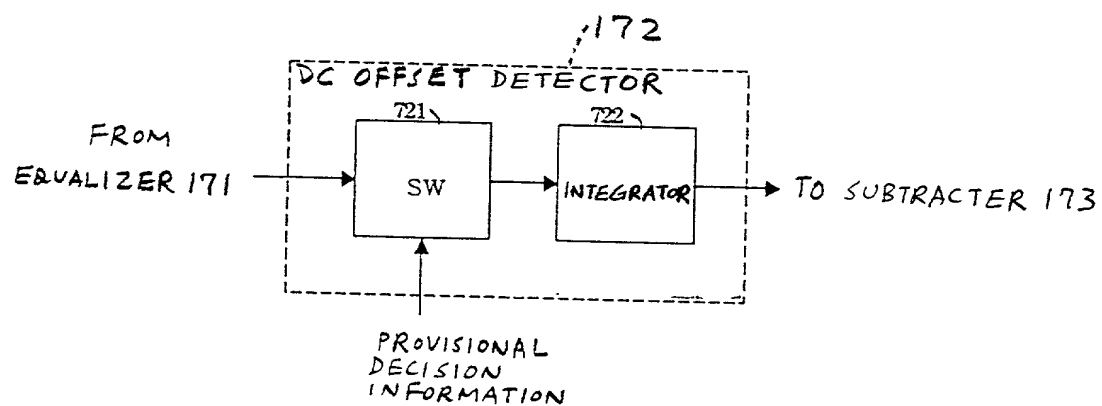


FIG. 16

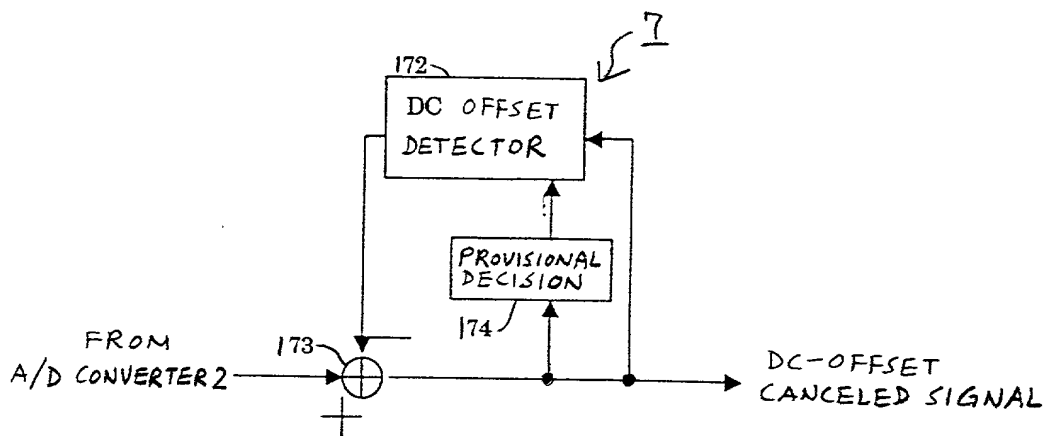


FIG. 17

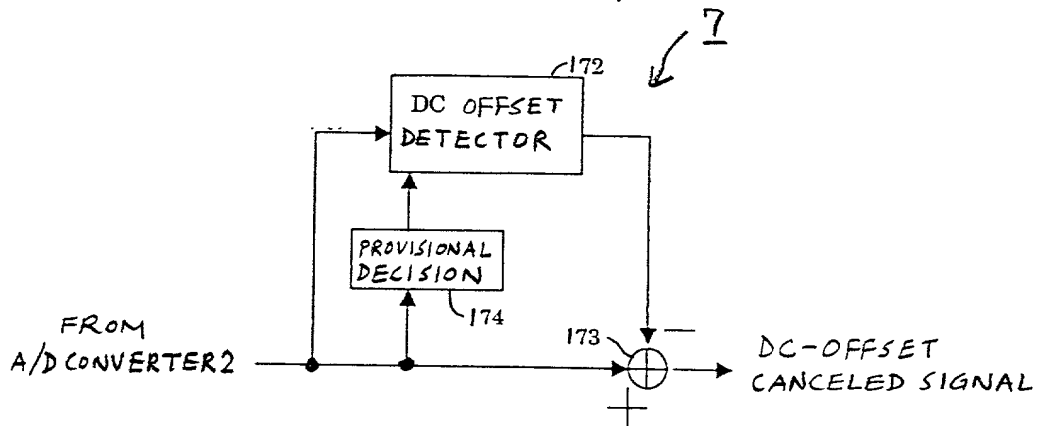


FIG. 18

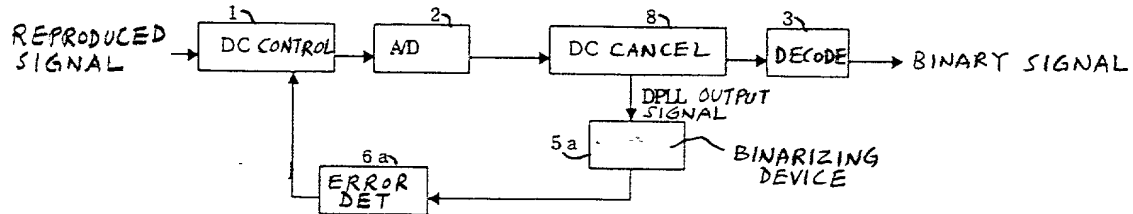


FIG. 19

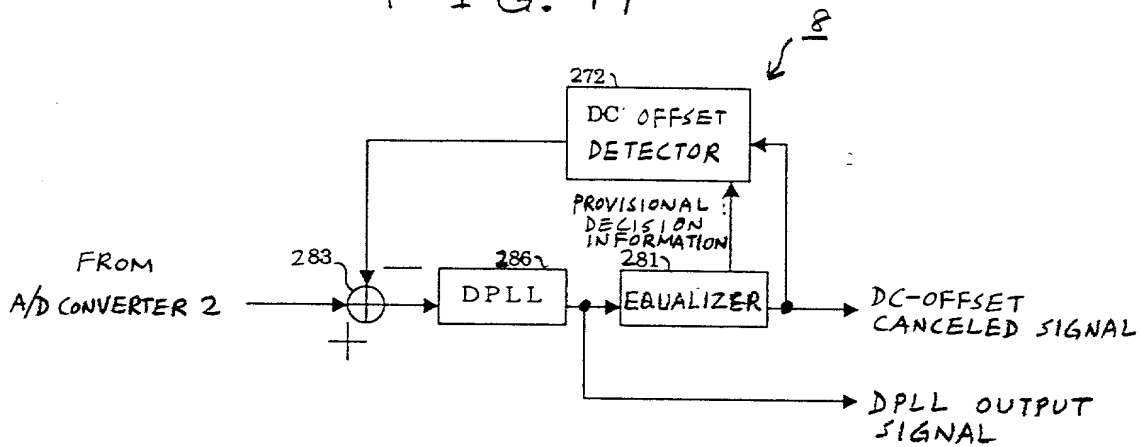


FIG. 20

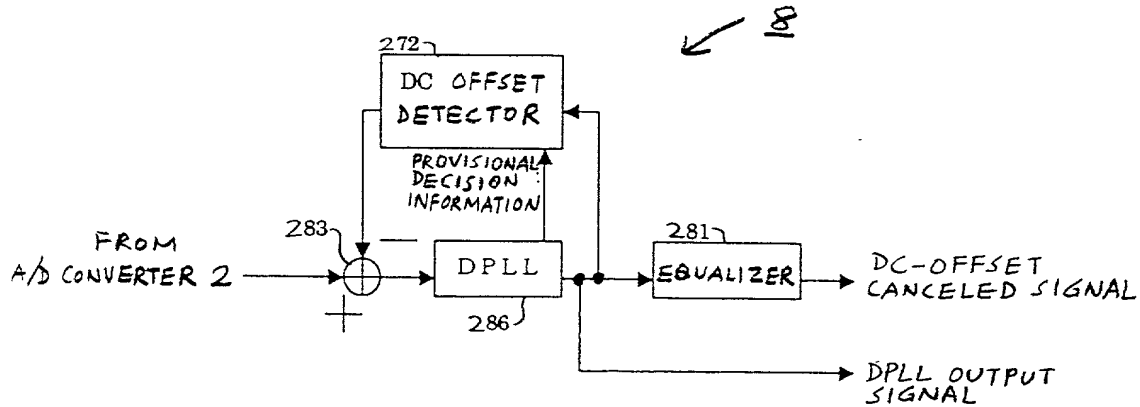
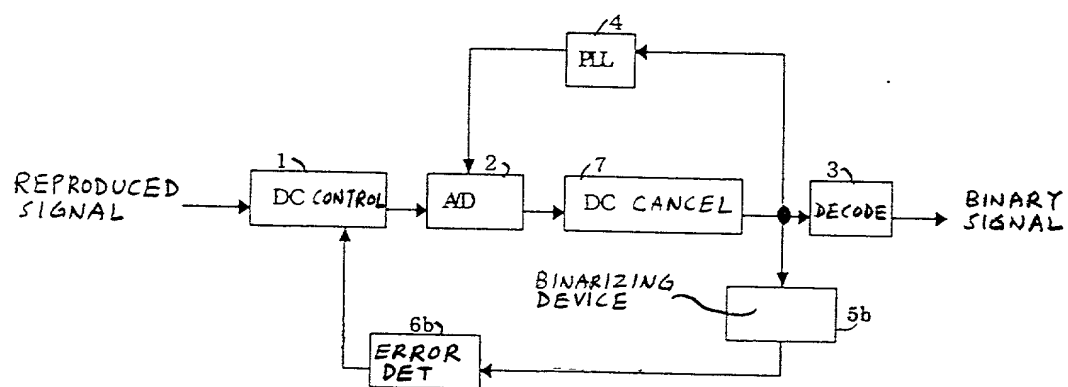


FIG. 21



The diagram shows a signal flow starting from a 'REPRODUCED SIGNAL' input into block 1, 'DC CONTROL'. The output of block 1 goes to block 2, 'AD'. Block 2 has a feedback loop from block 4, 'FLL', which also receives input from block 3, 'DECODE'. The output of block 2 goes to block 7, 'DC CANCEL'. The output of block 7 goes to block 3, 'DECODE'. The output of block 3 is the 'BINARY SIGNAL'. There is also a feedback loop from the output of block 3 through block 6c, 'ERROR DET', back to block 1.

A block diagram of a signal processing system. The input is labeled "REPRODUCED SIGNAL". It enters a block labeled "AD" (with a handwritten "2" above it). The output of "AD" goes to a block labeled "DC CONTROL" (with a handwritten "9" above it). From "DC CONTROL", the signal goes to a block labeled "DC CANCEL" (with a handwritten "7" above it). The output of "DC CANCEL" goes to a block labeled "DECODE" (with a handwritten "3" above it). The output of "DECODE" is labeled "BINARY SIGNAL". There are two feedback loops. One loop goes from the output of "DECODE" back to the input of "AD" (labeled with a handwritten "4" above the line). The other loop goes from the output of "DECODE" through a block labeled "BINARIZING DEVICE" (with a handwritten "5d" next to it) to a block labeled "ERROR DET" (with a handwritten "6d" below it). The output of "ERROR DET" goes back to the input of "DC CONTROL".

FIG. 24

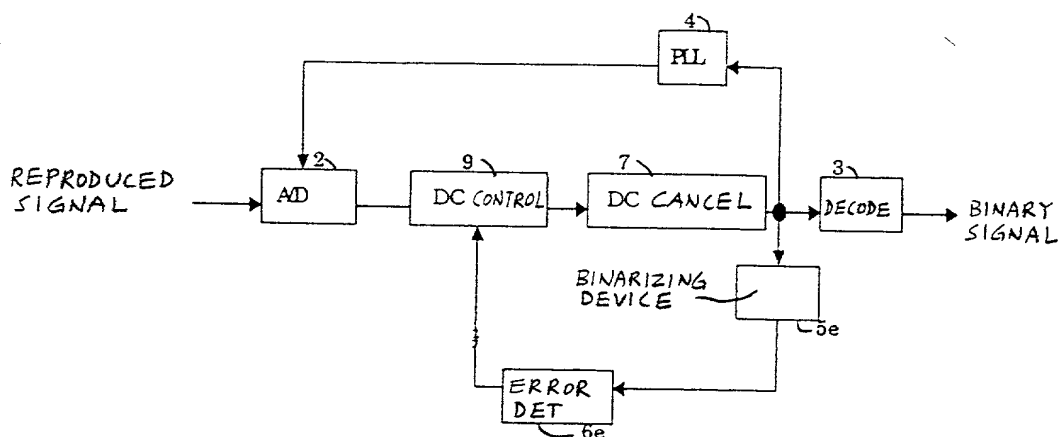


FIG. 25

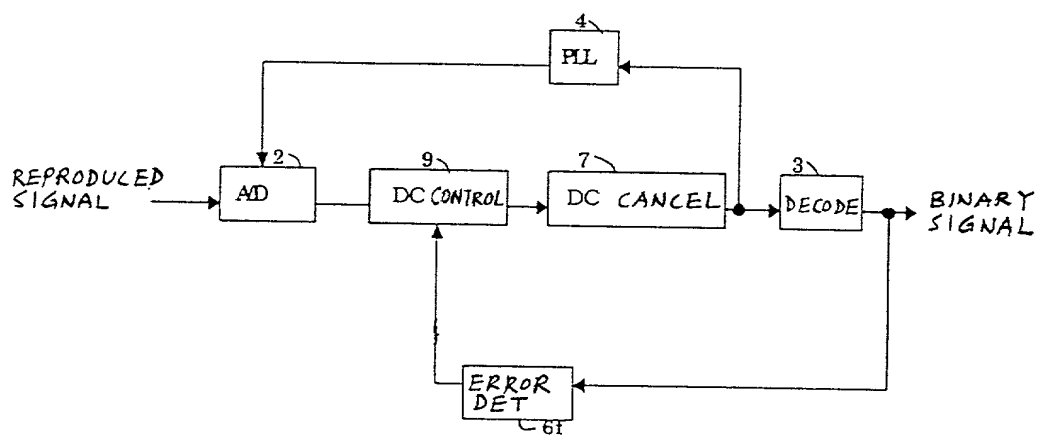


FIG. 26

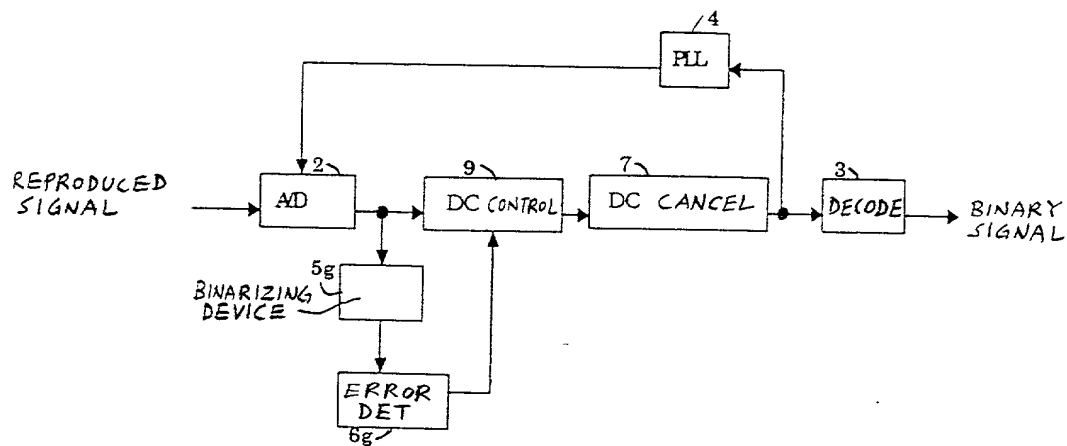


FIG. 27

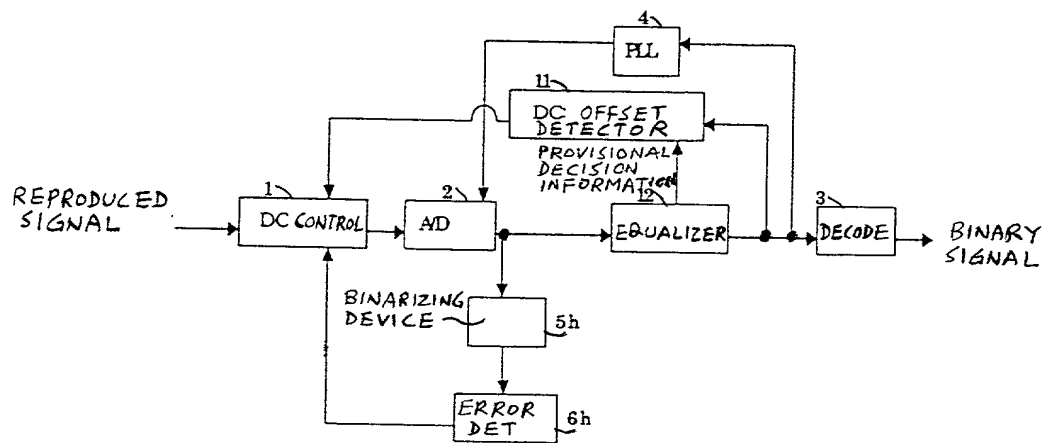


FIG. 28

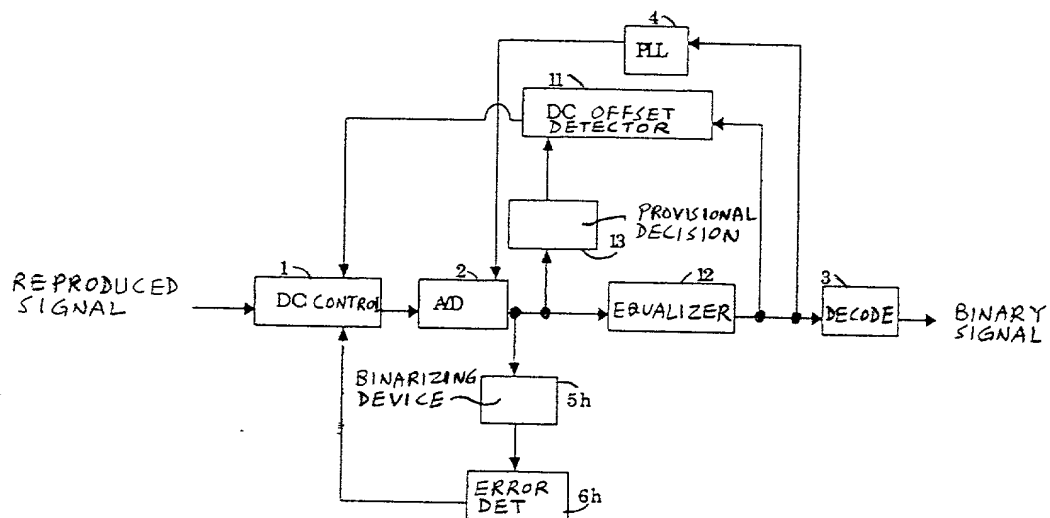


FIG. 29

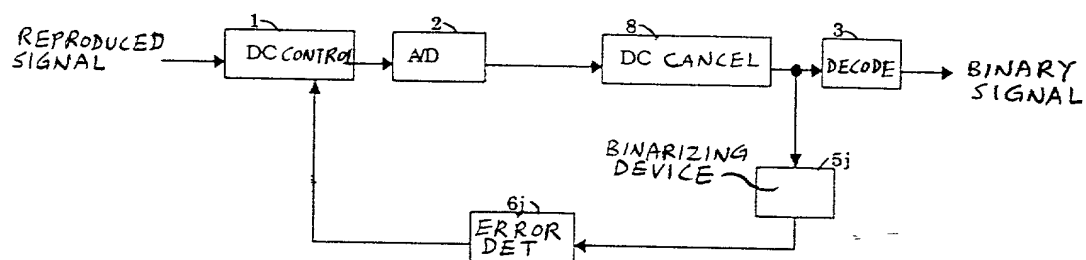


FIG. 30

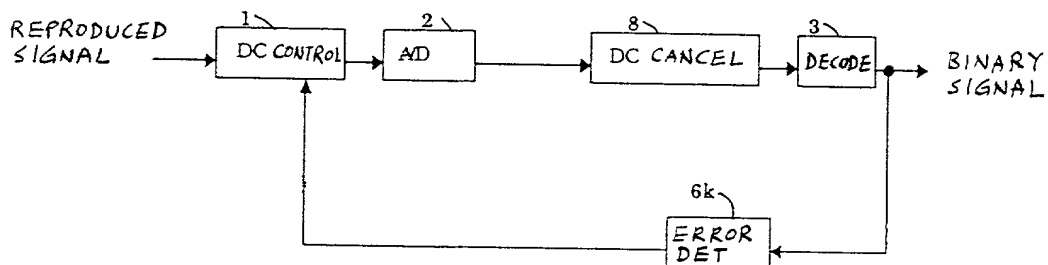


FIG. 31

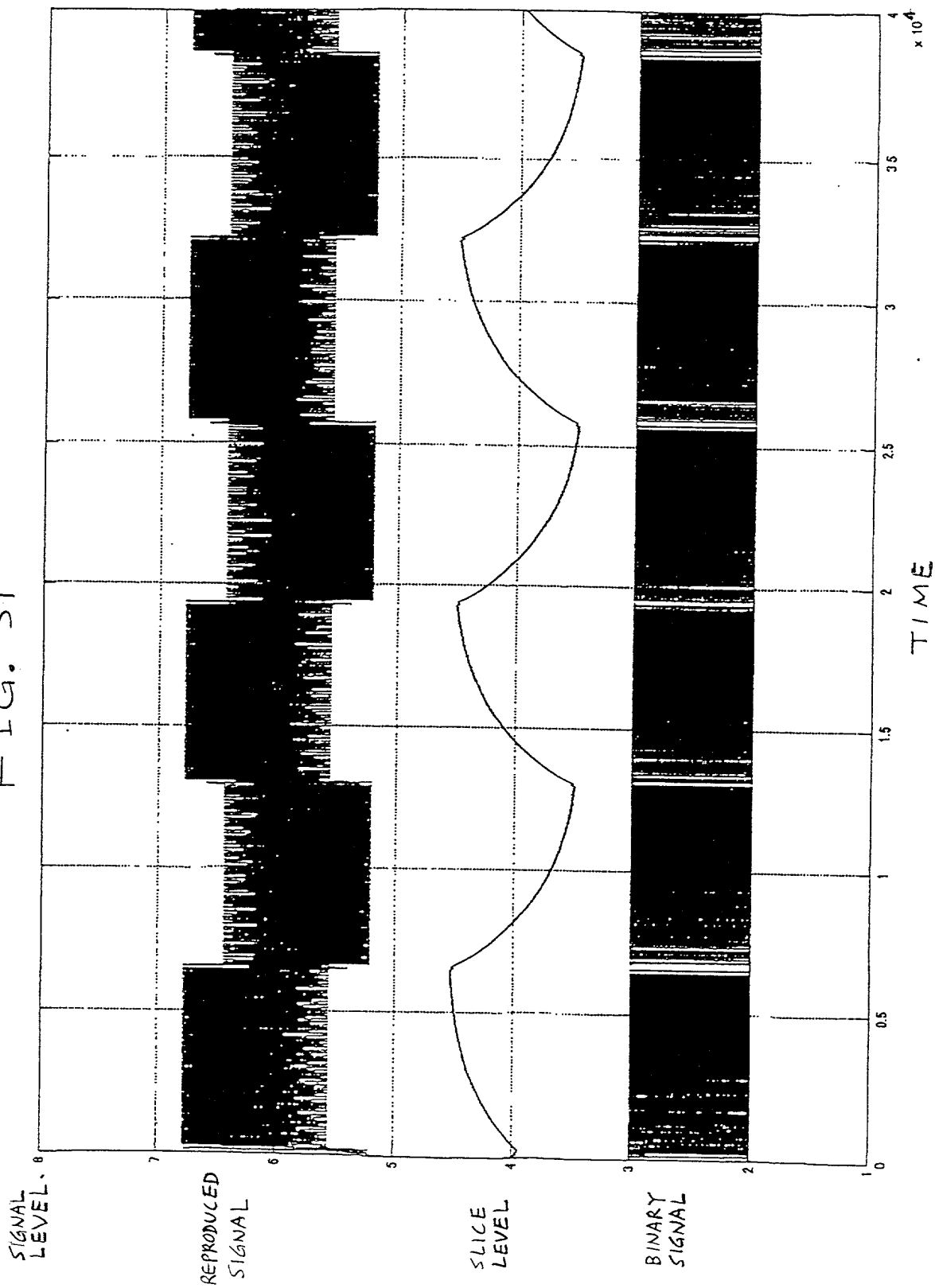


FIG. 32

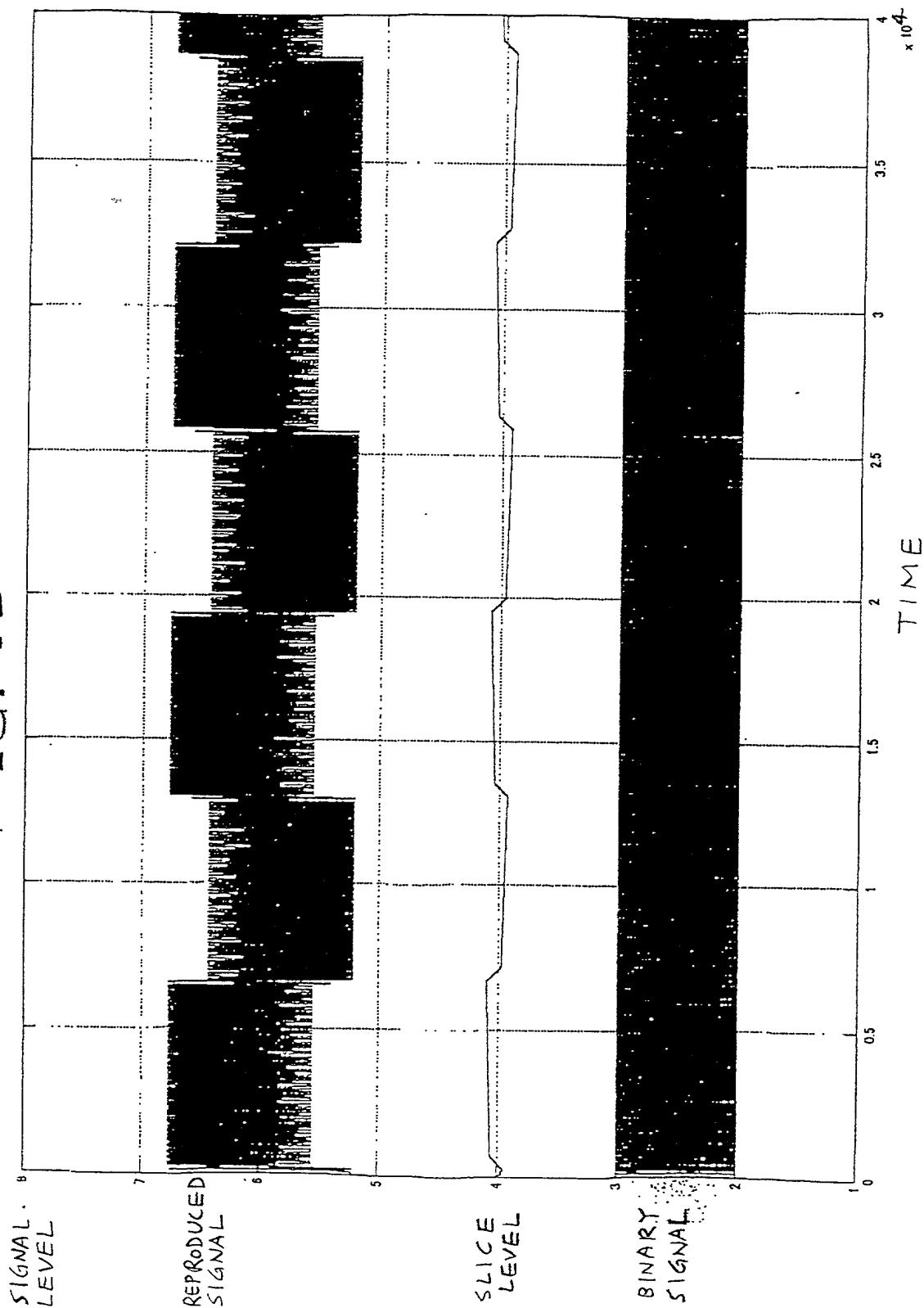


FIG. 33

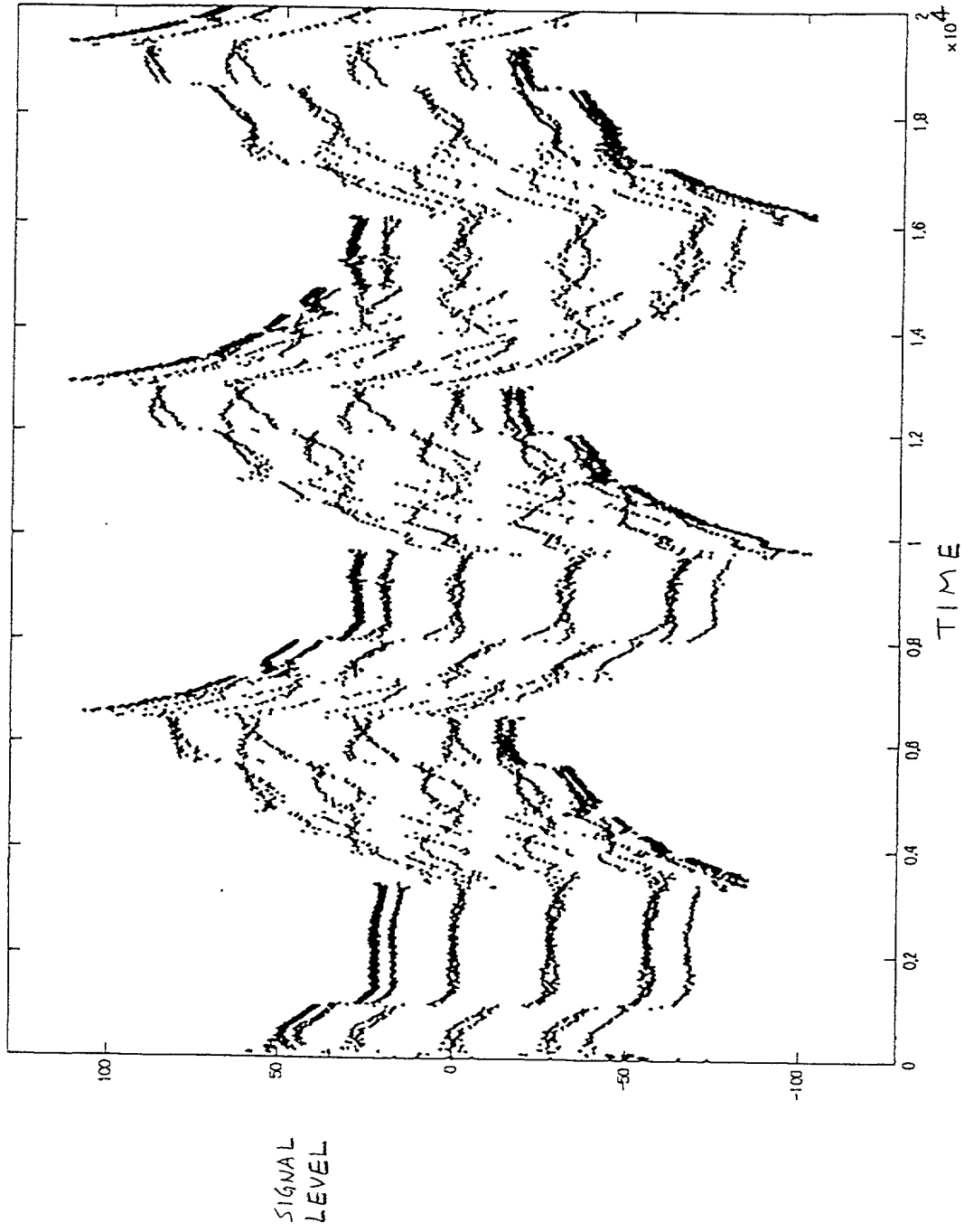
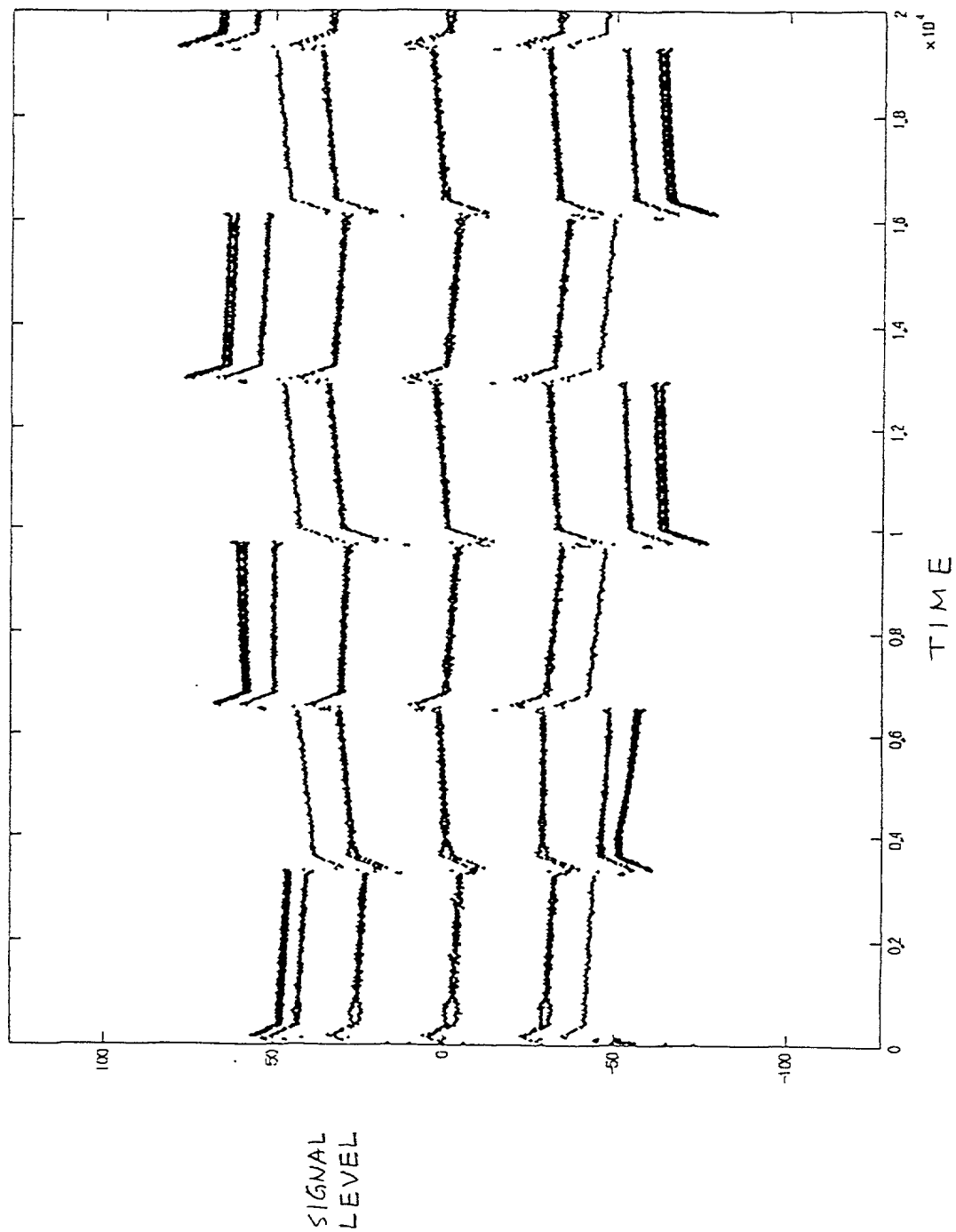
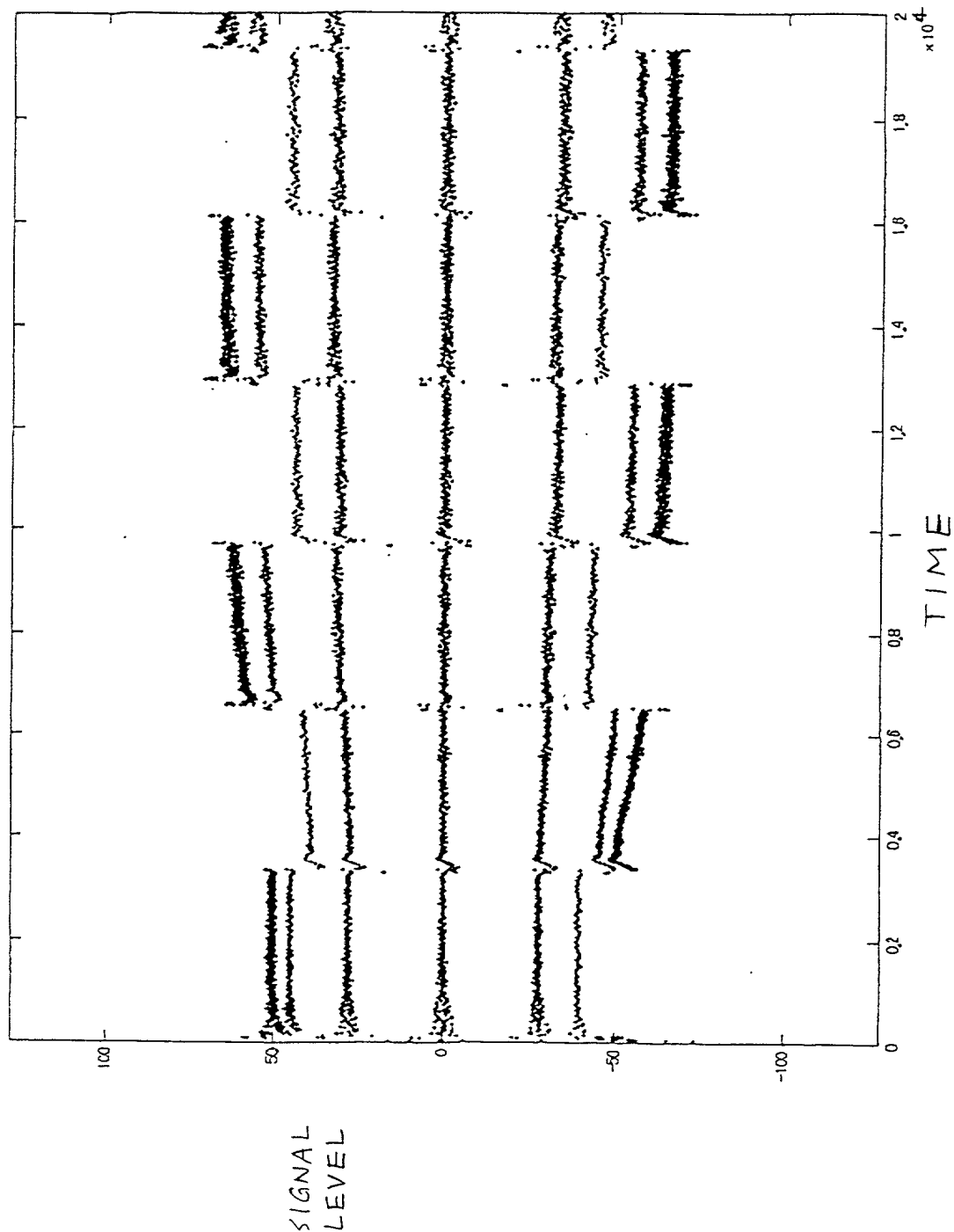


FIG. 34



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FIG. 35



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FIG. 36

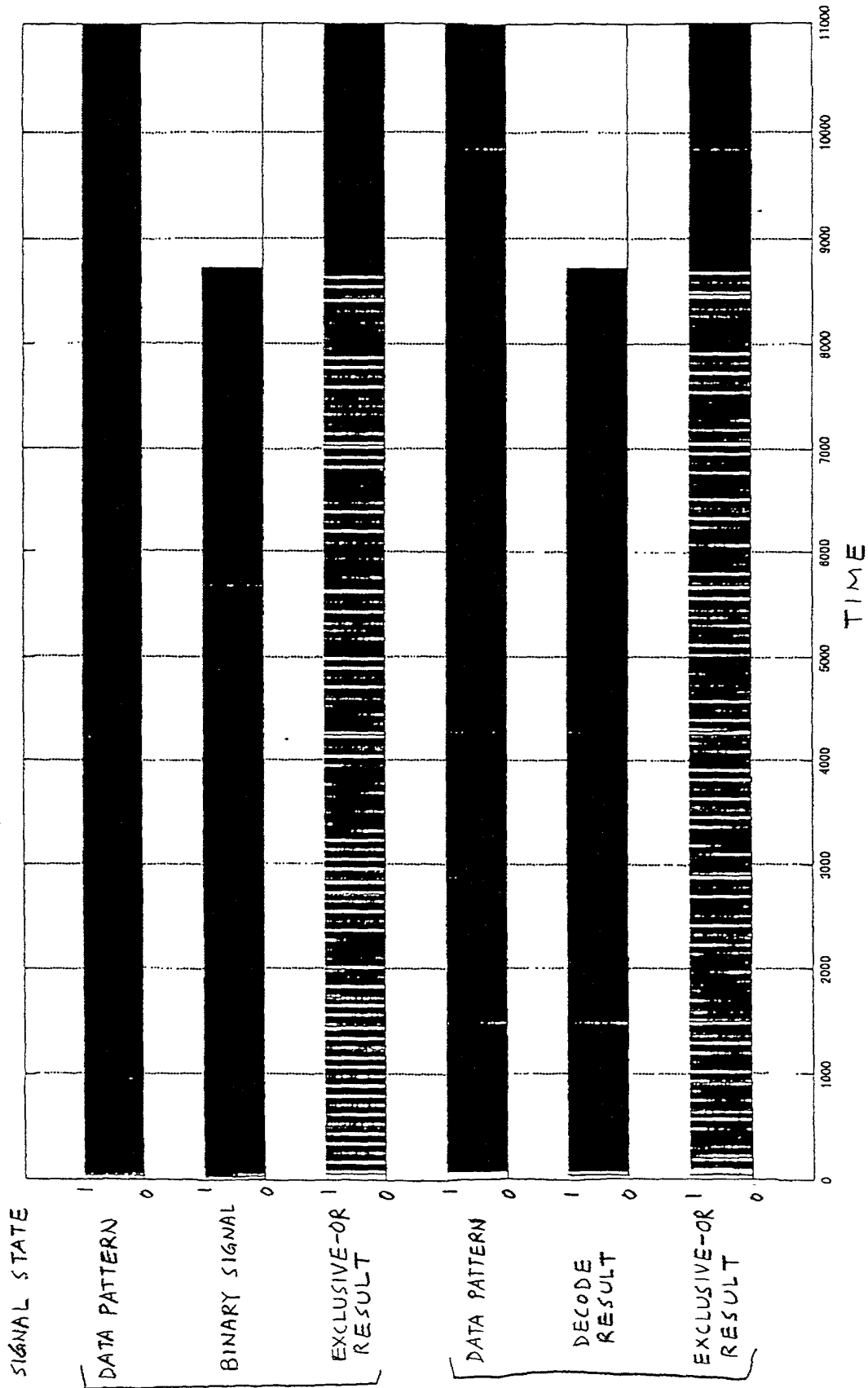


FIG. 37

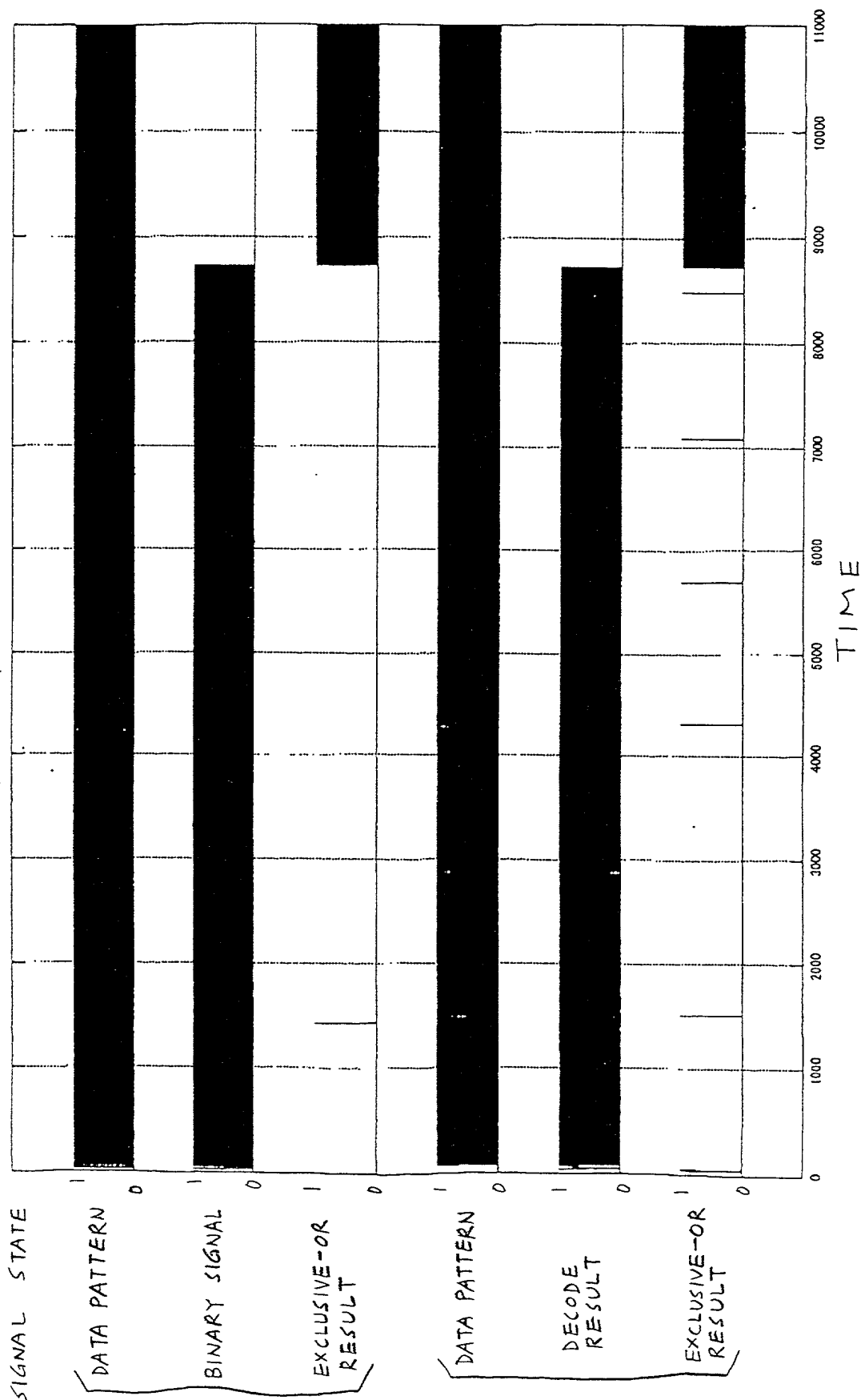


FIG. 38

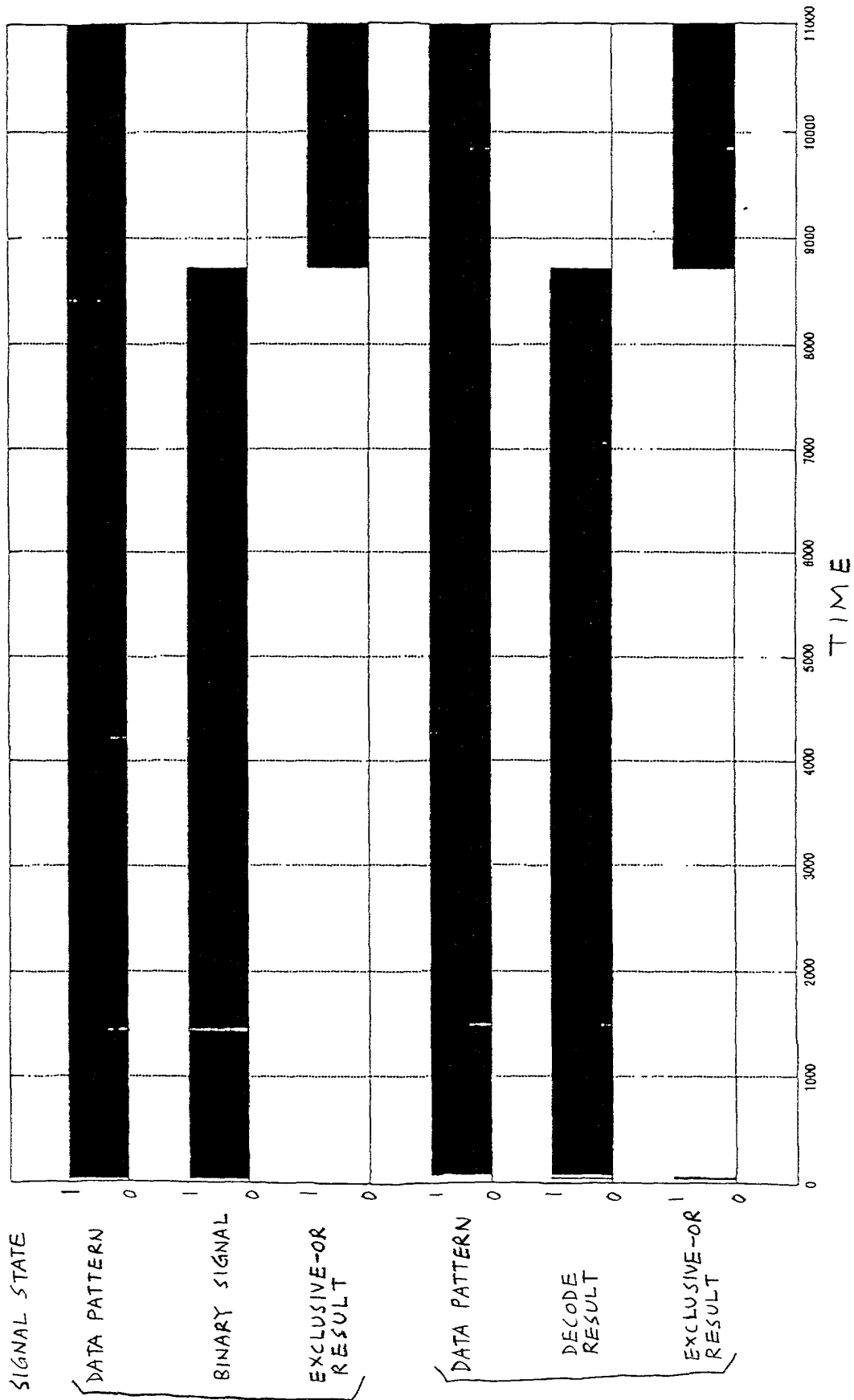


FIG. 39

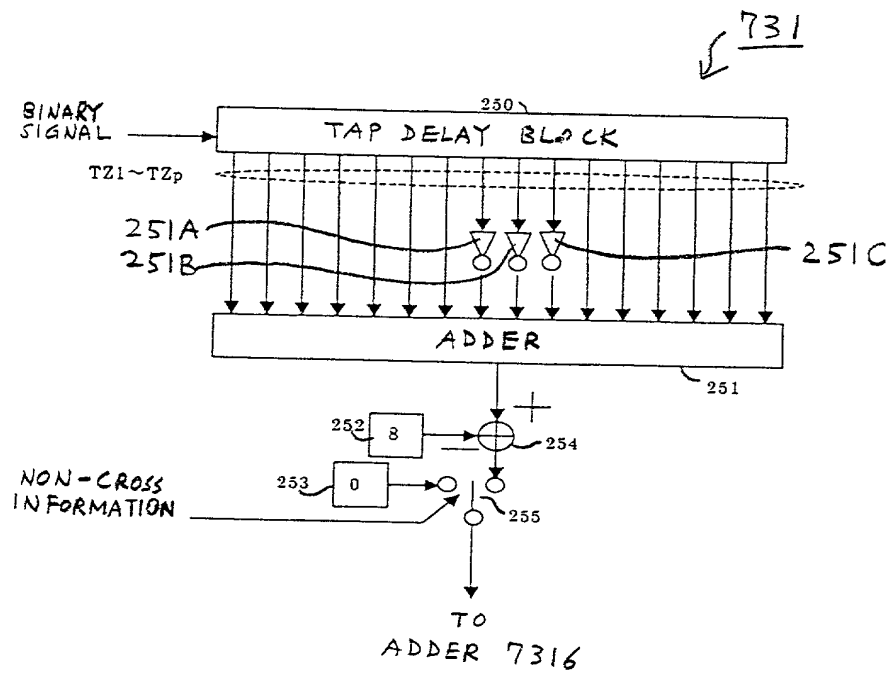


FIG. 40

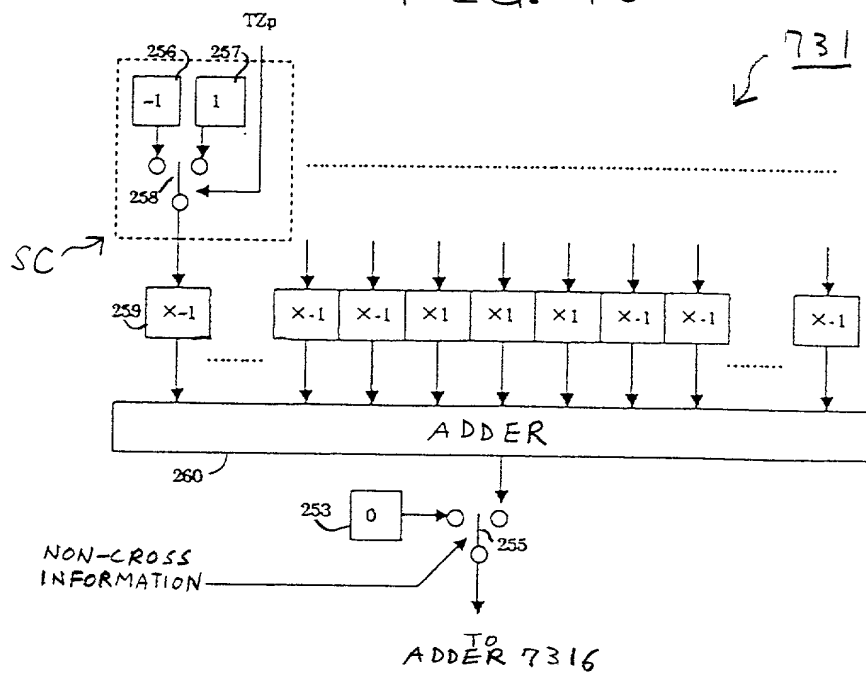


FIG. 41

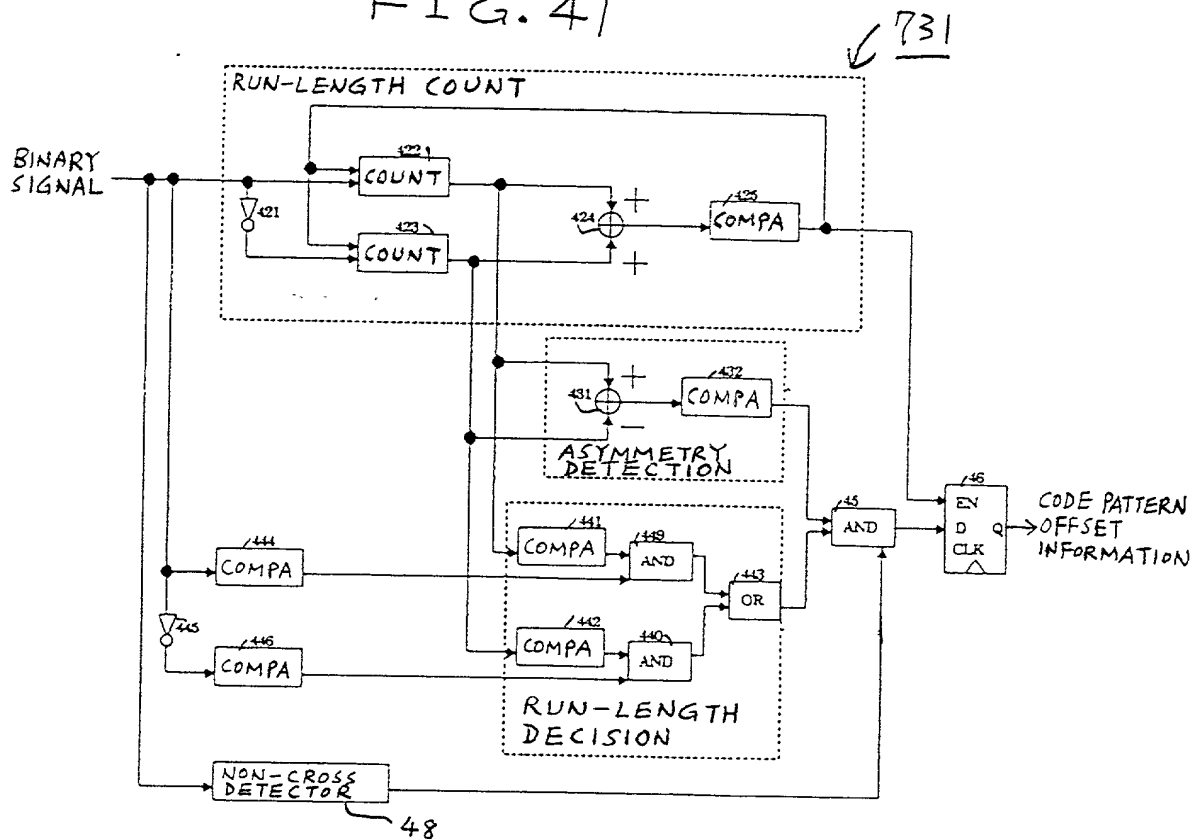


FIG. 42

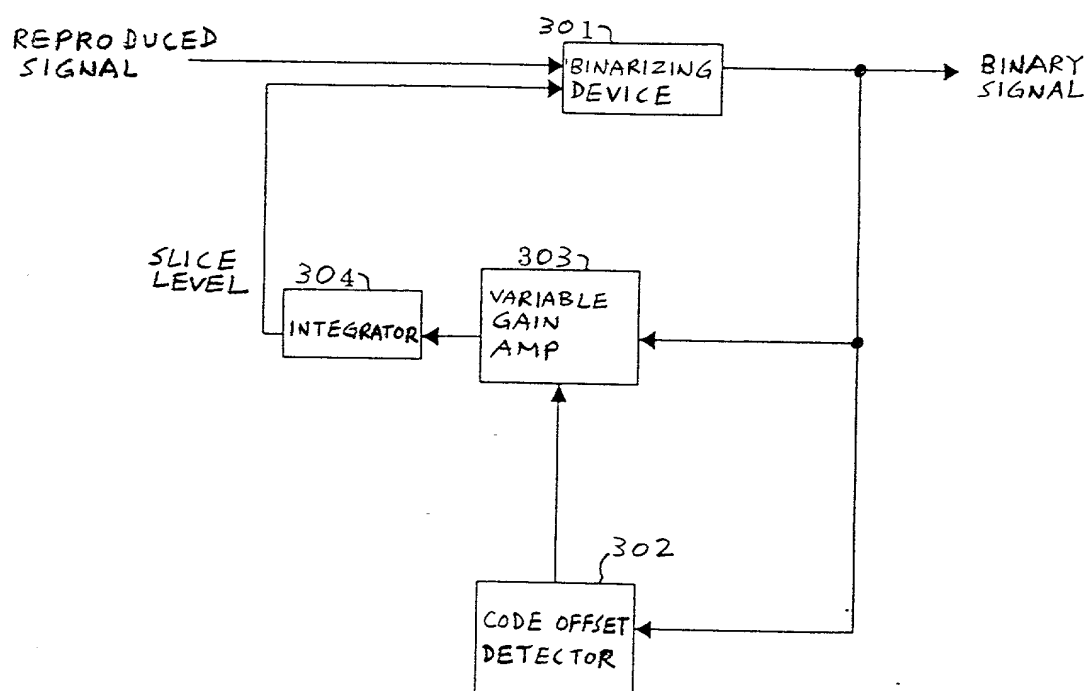


FIG. 43

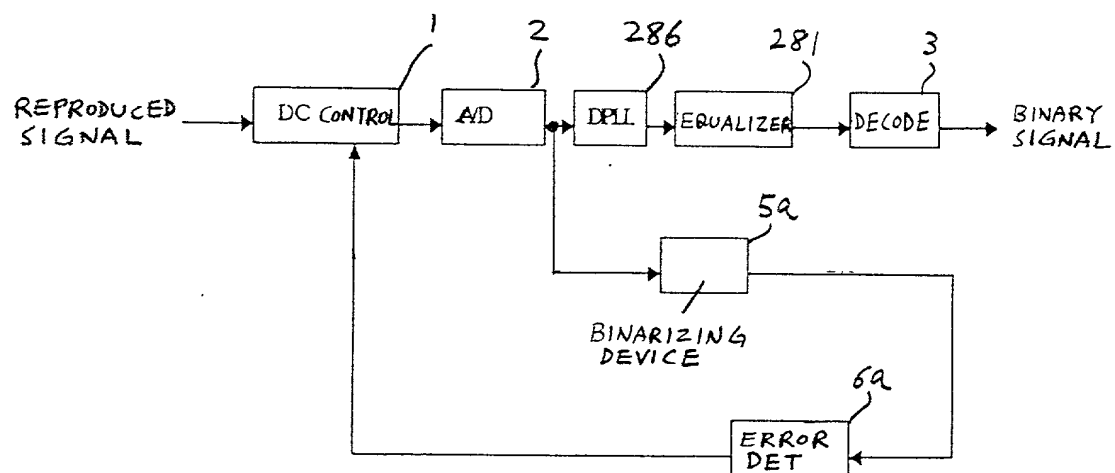


FIG. 44

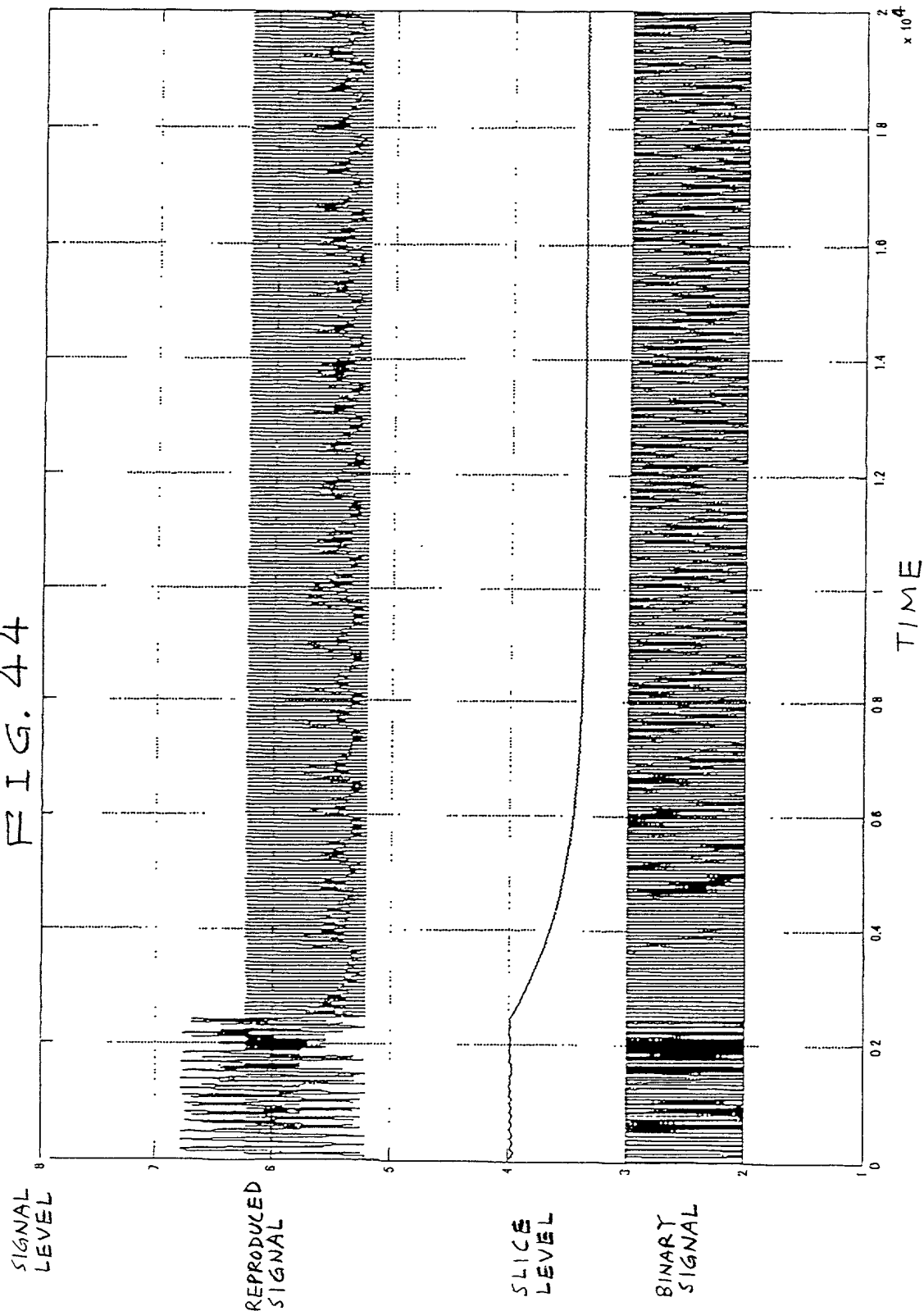
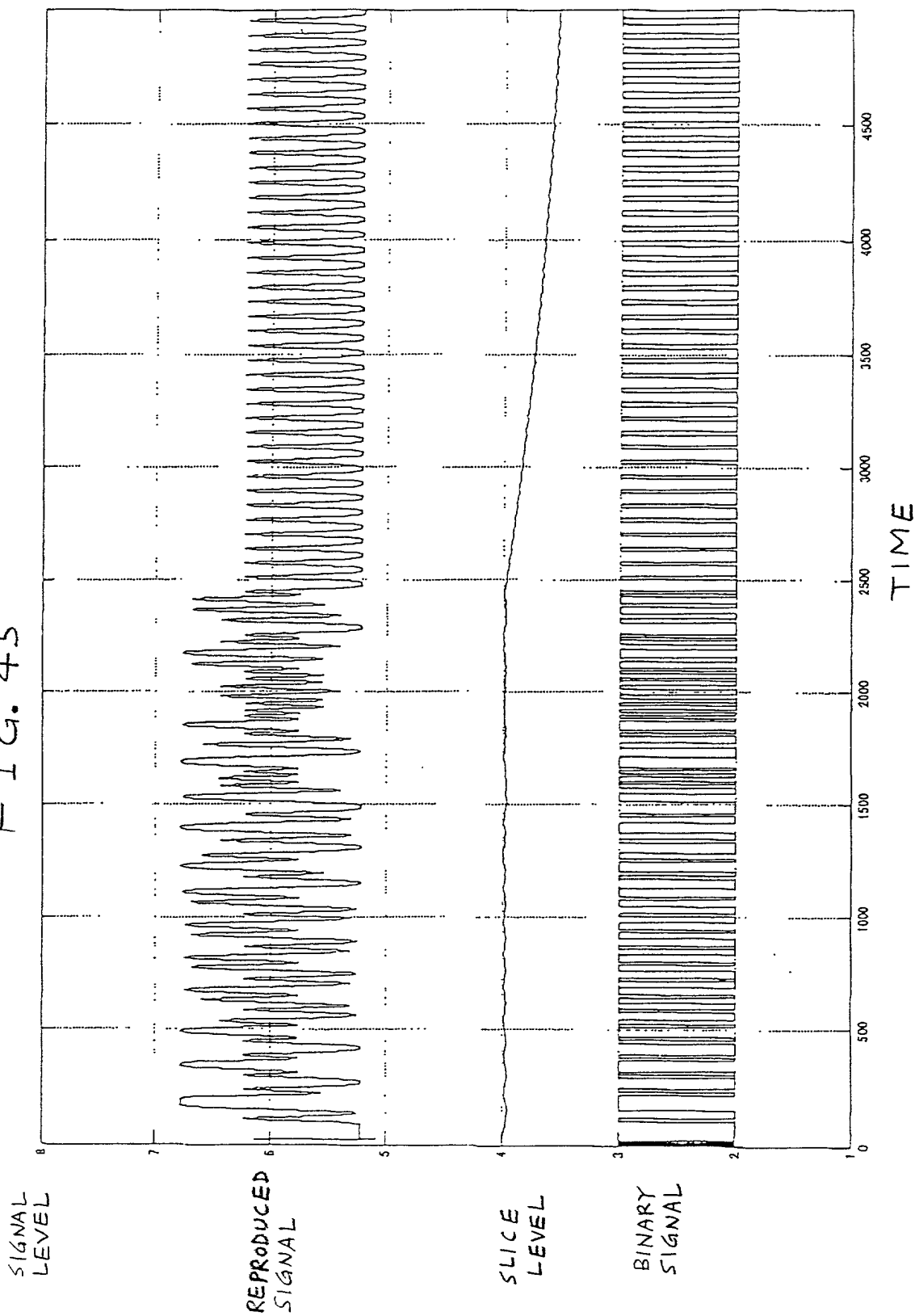
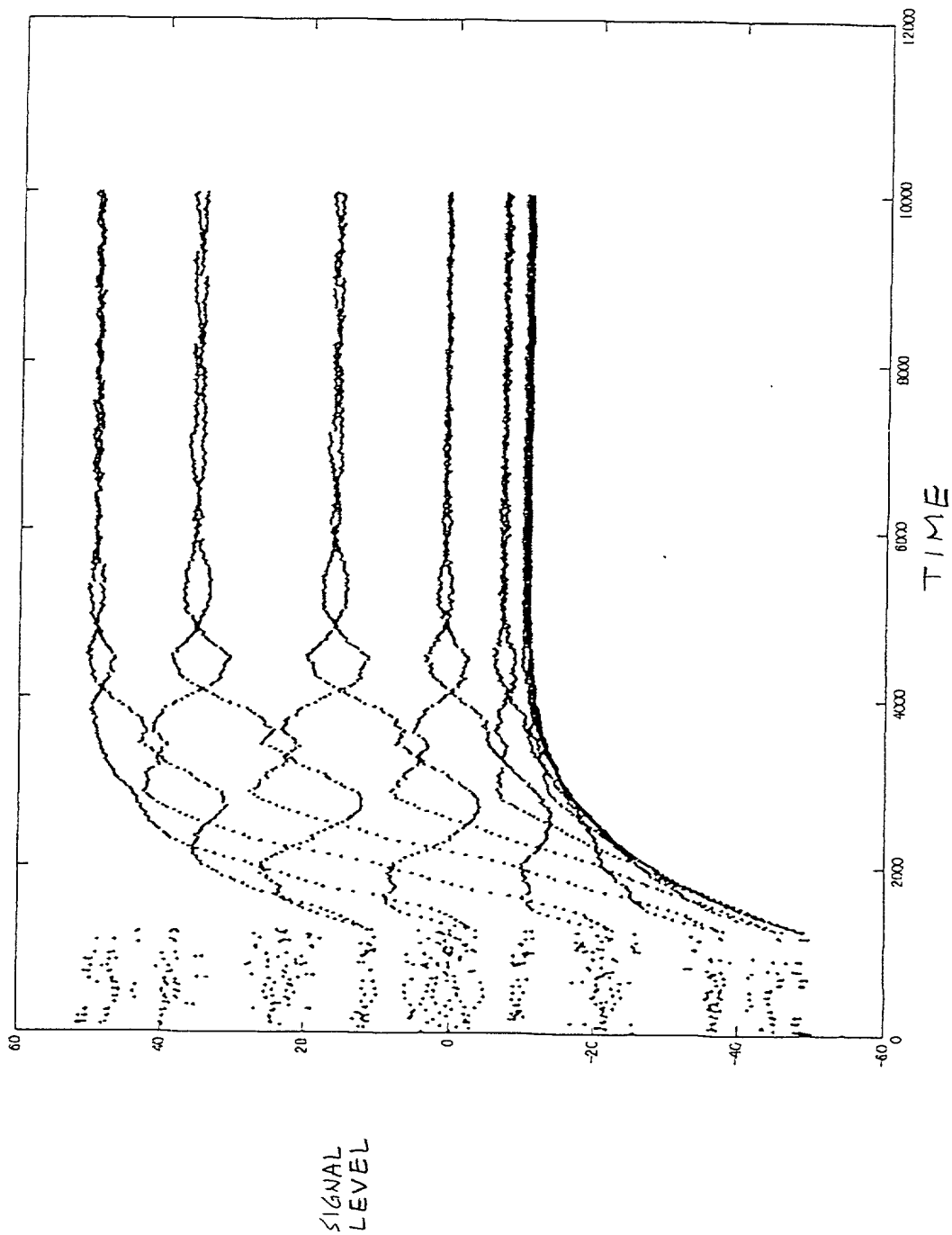


FIG. 45



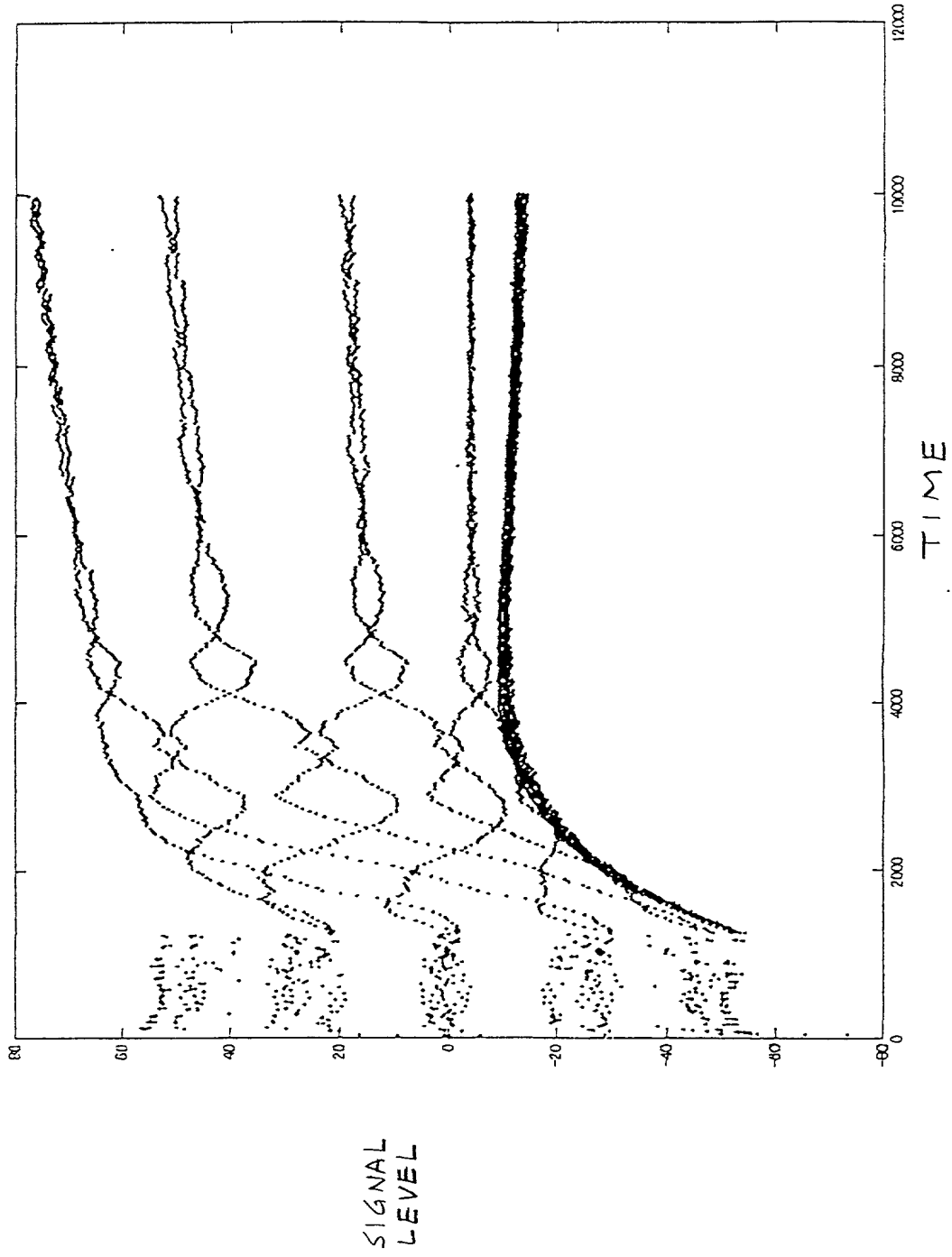
202220" 454E800T

FIG. 46



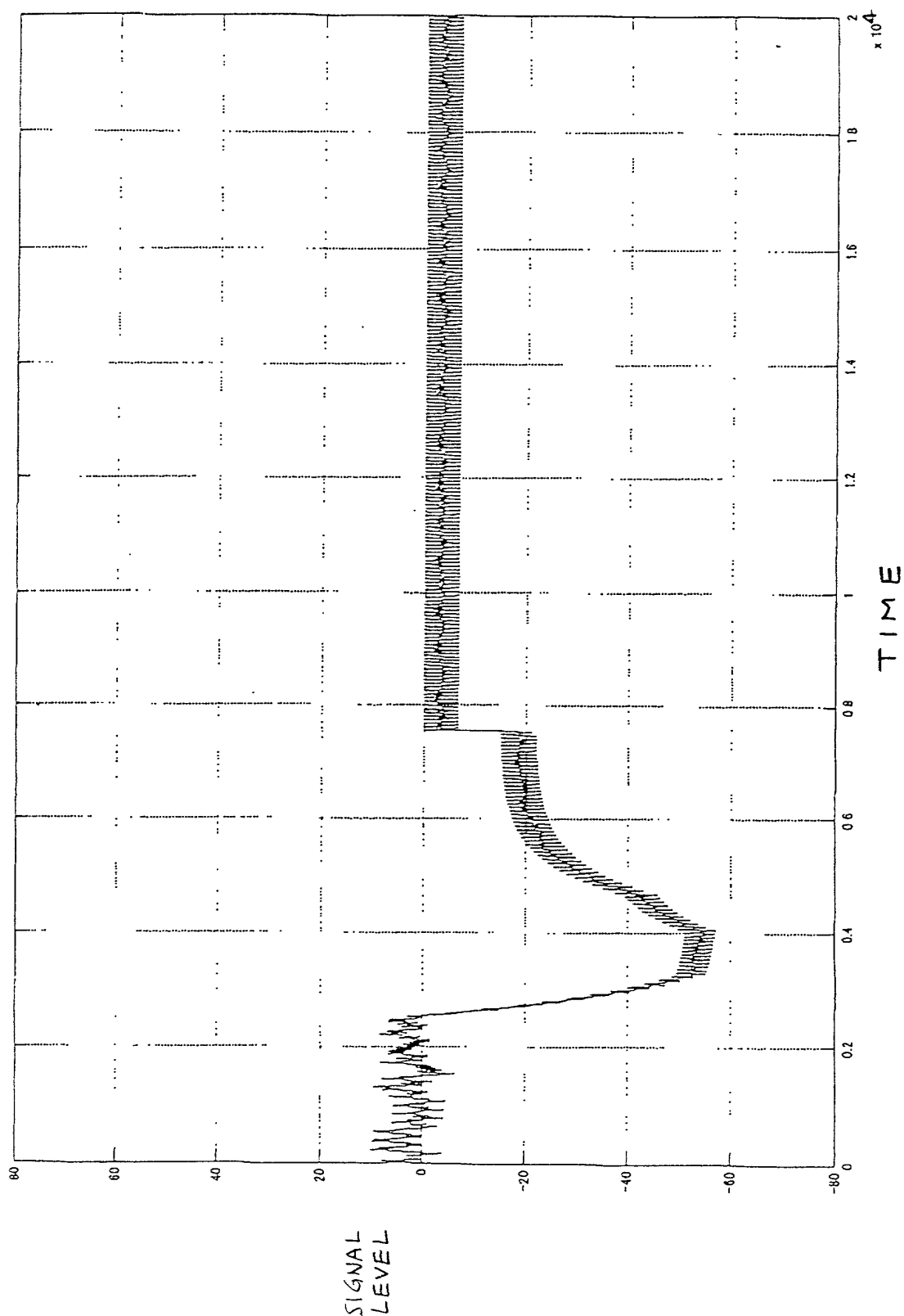
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FIG. 47



20/220" 464E8007

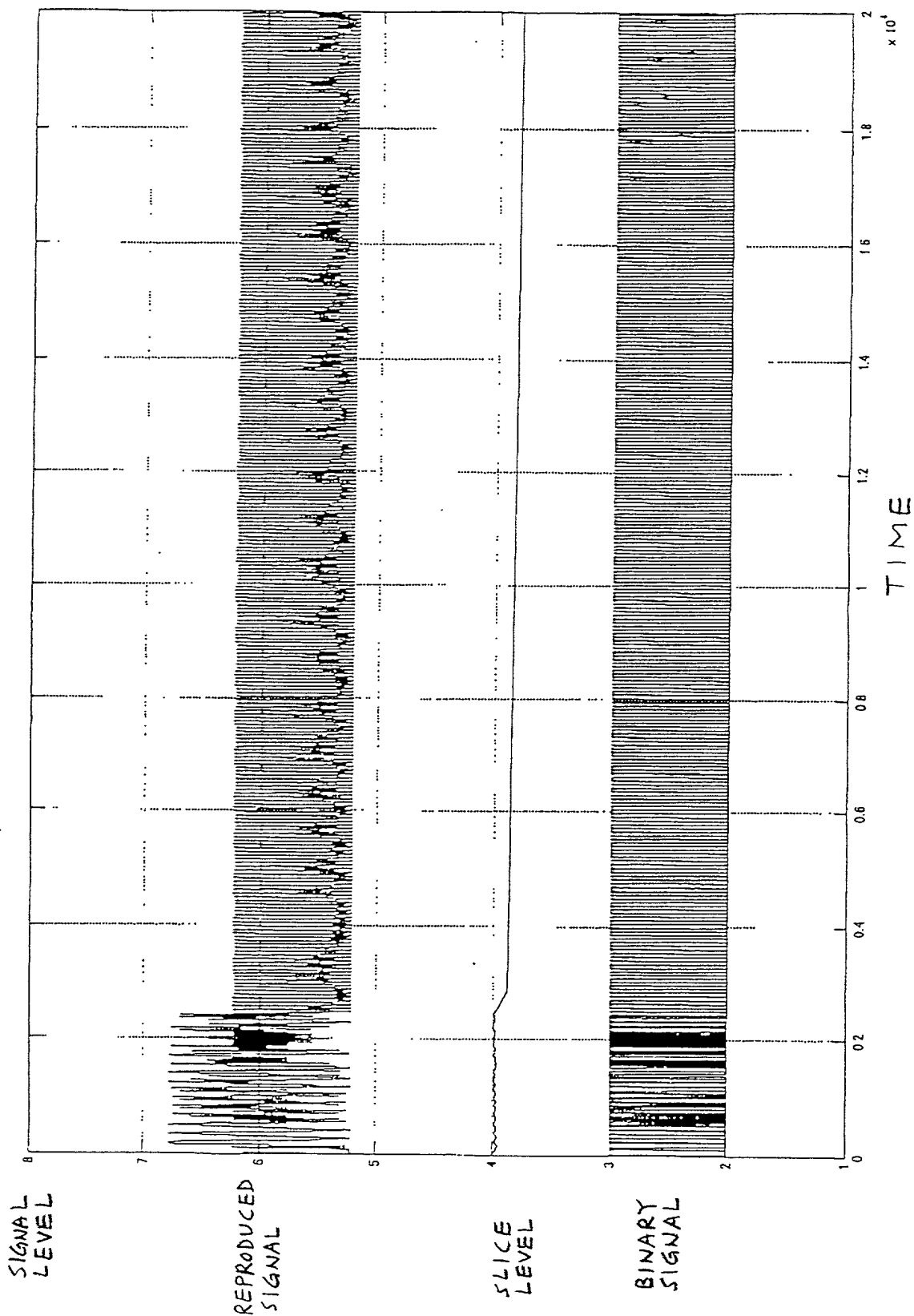
FIG. 48



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20/220" 464E800T

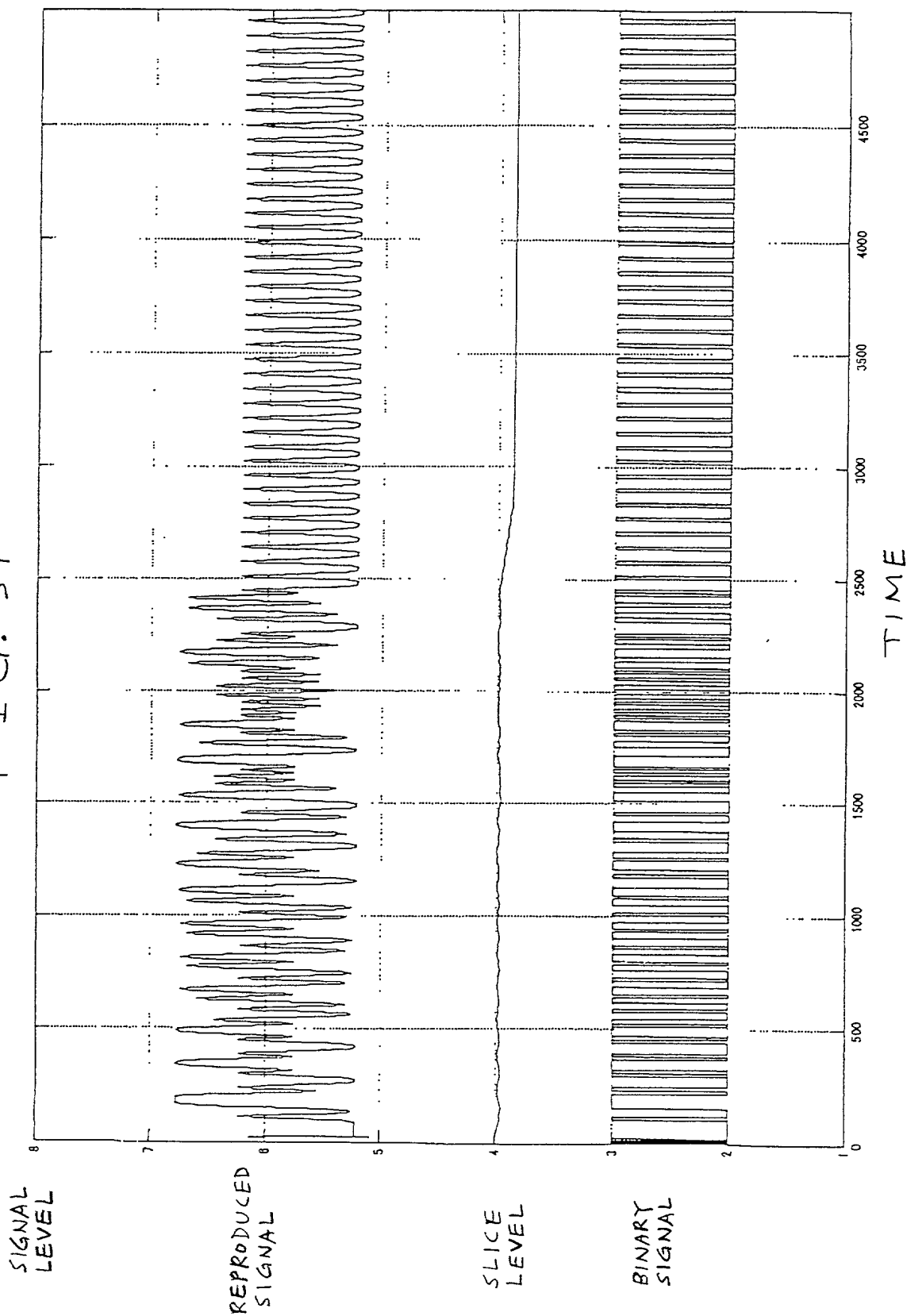
FIG. 50



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20220" 464E300T

FIG. 51



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20/220"464E800F

FIG. 52

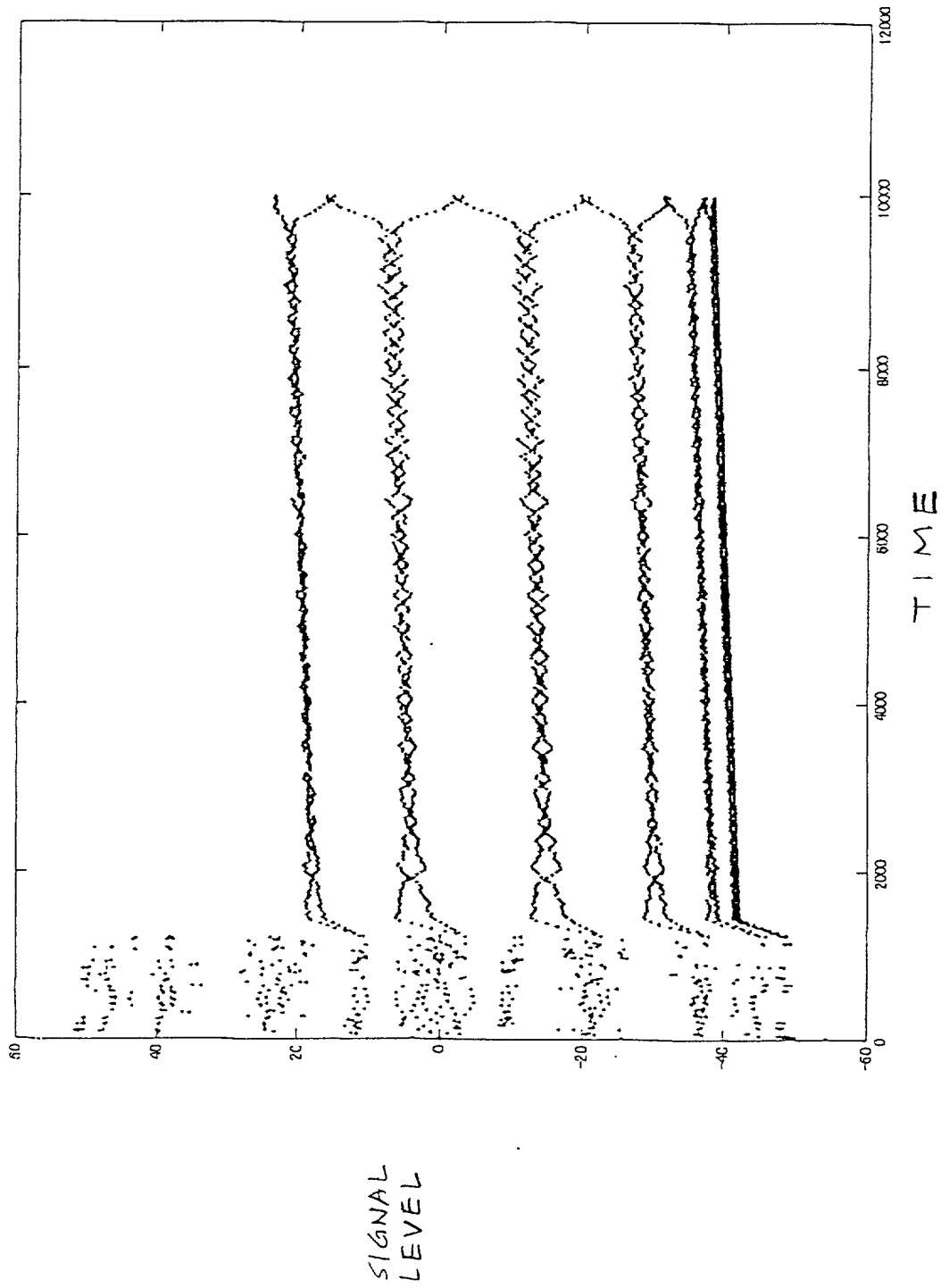
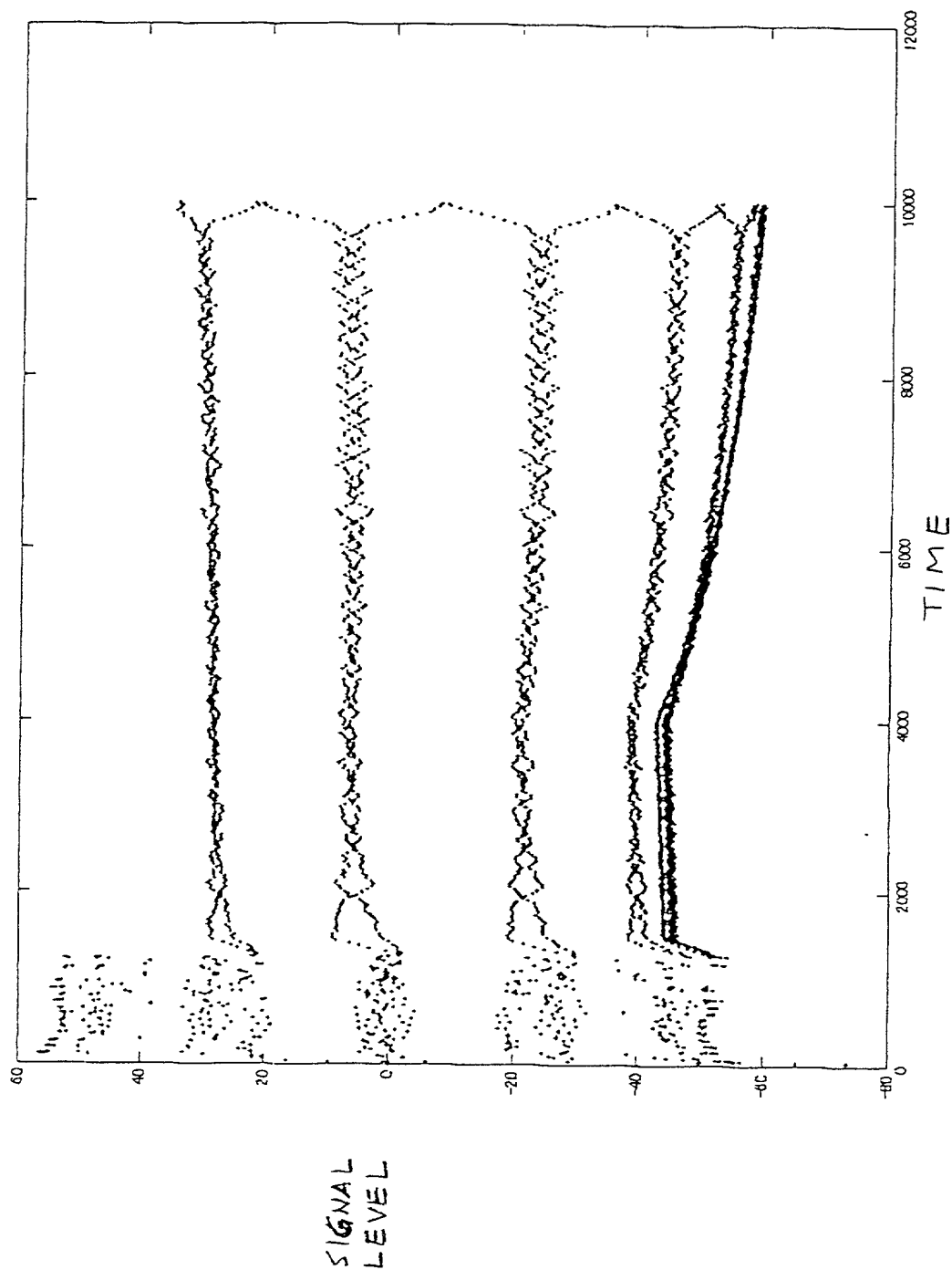
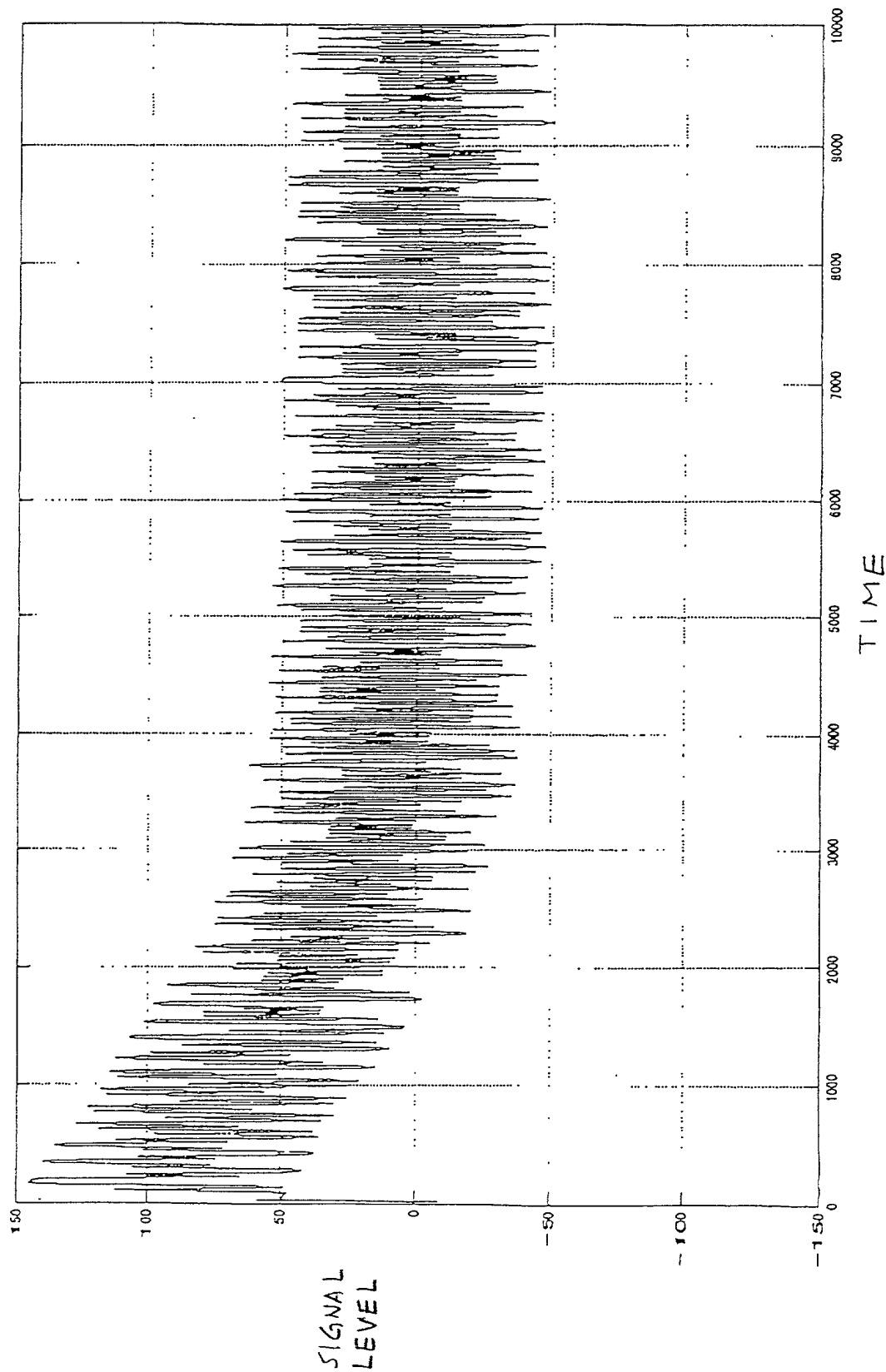


FIG. 53



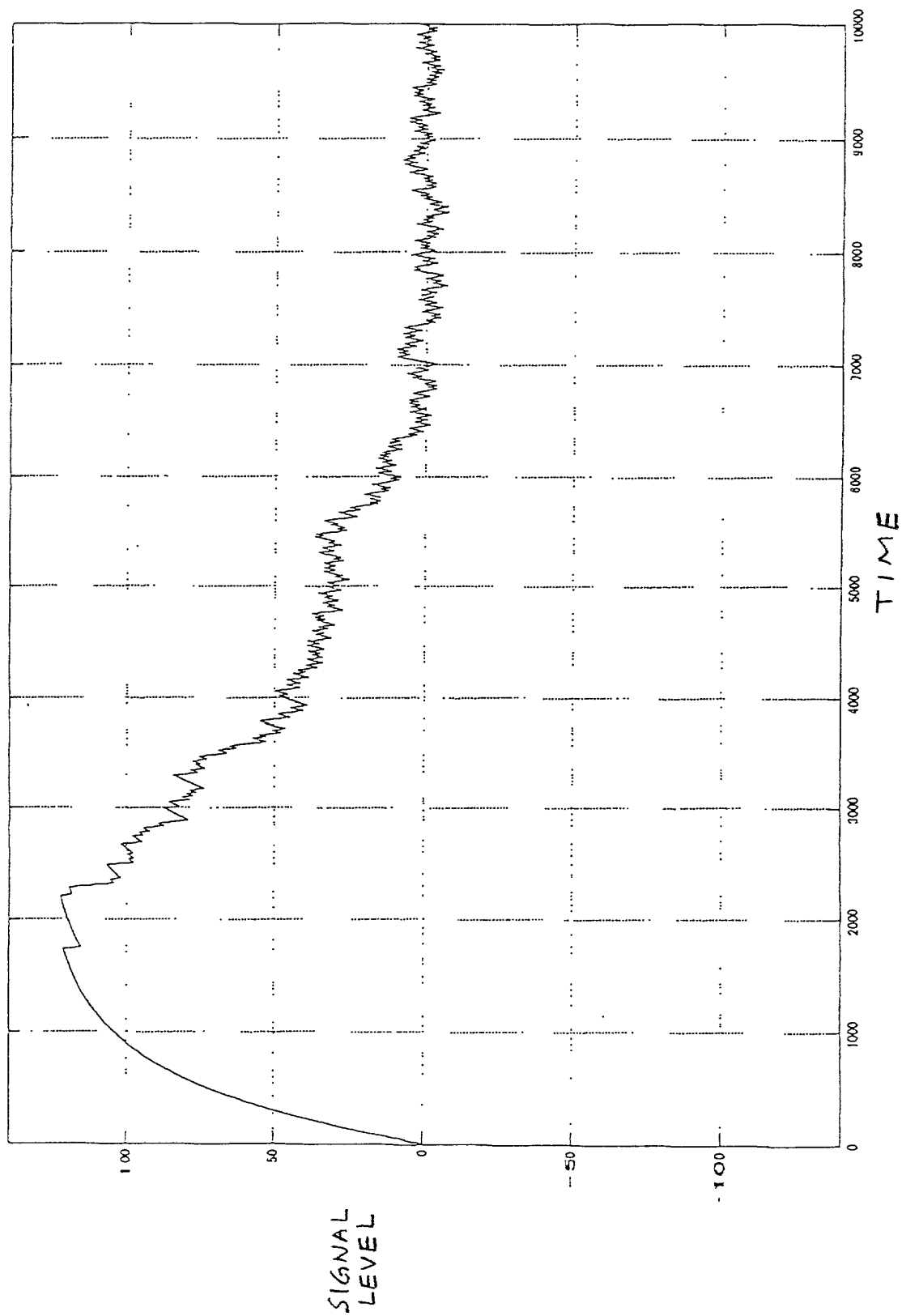
202220"464E800F

FIG. 54



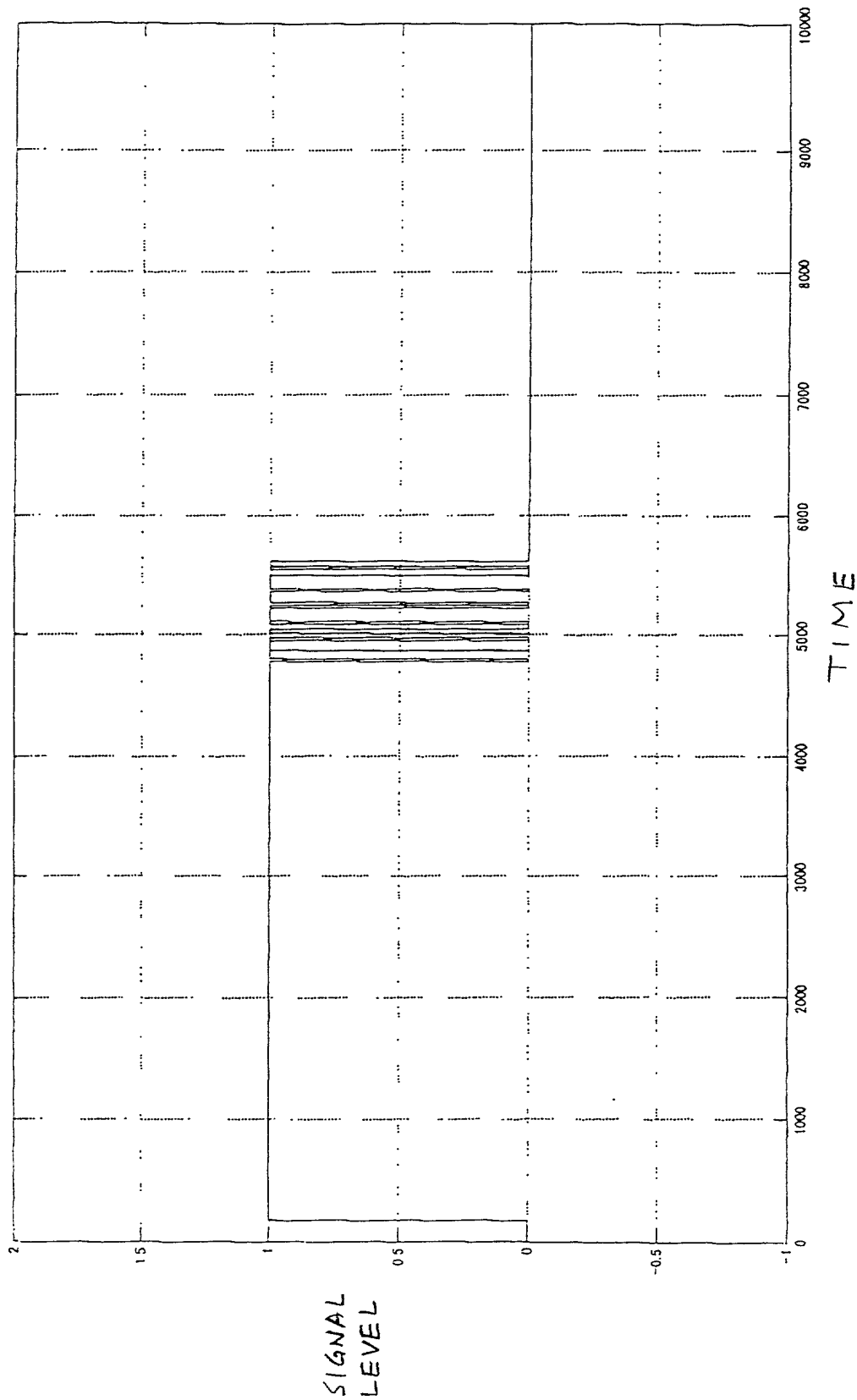
20/220" H6HE800F

FIG. 55



202220" 464E800F

FIG. 56



20220-154800T

FIG. 58

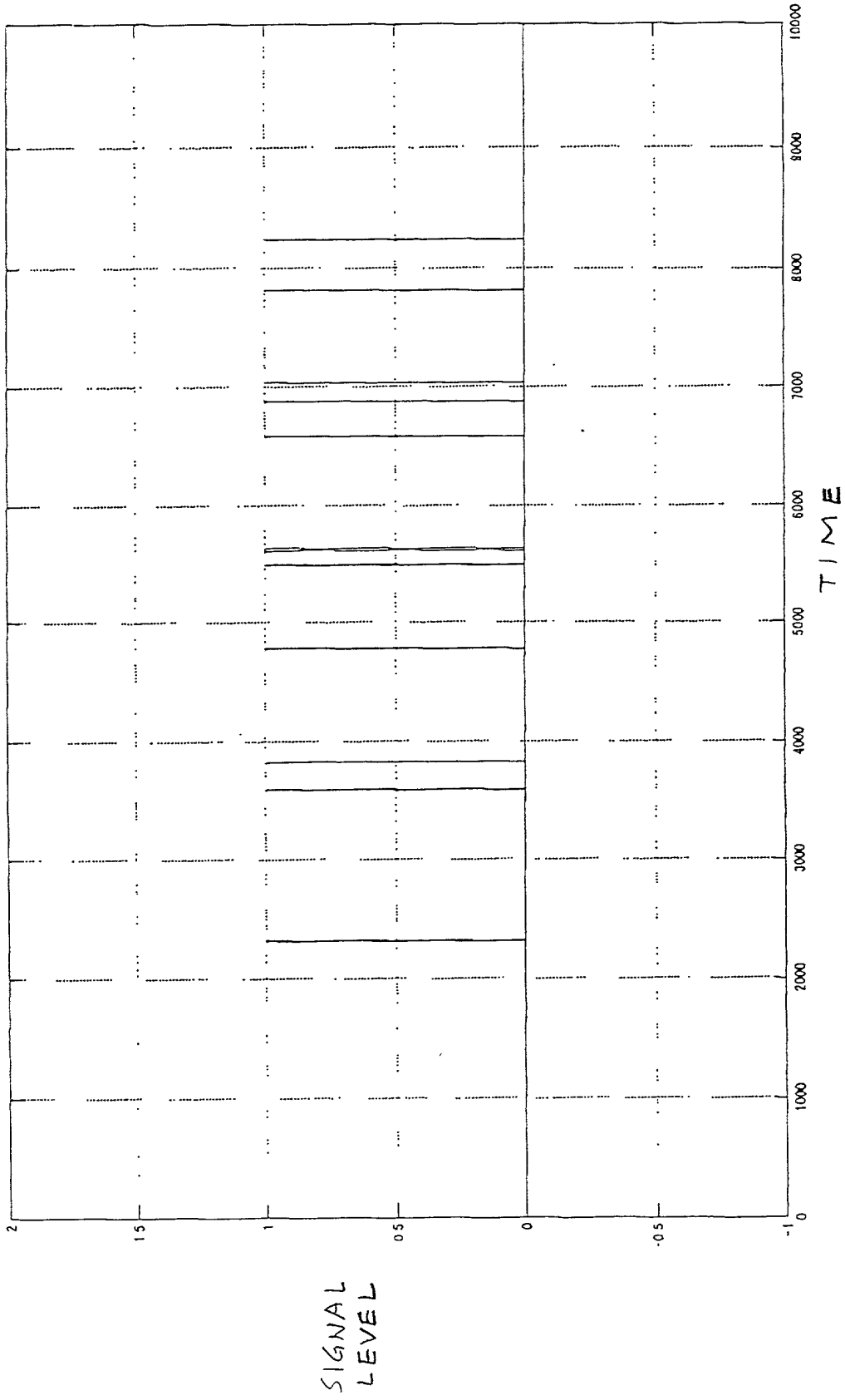
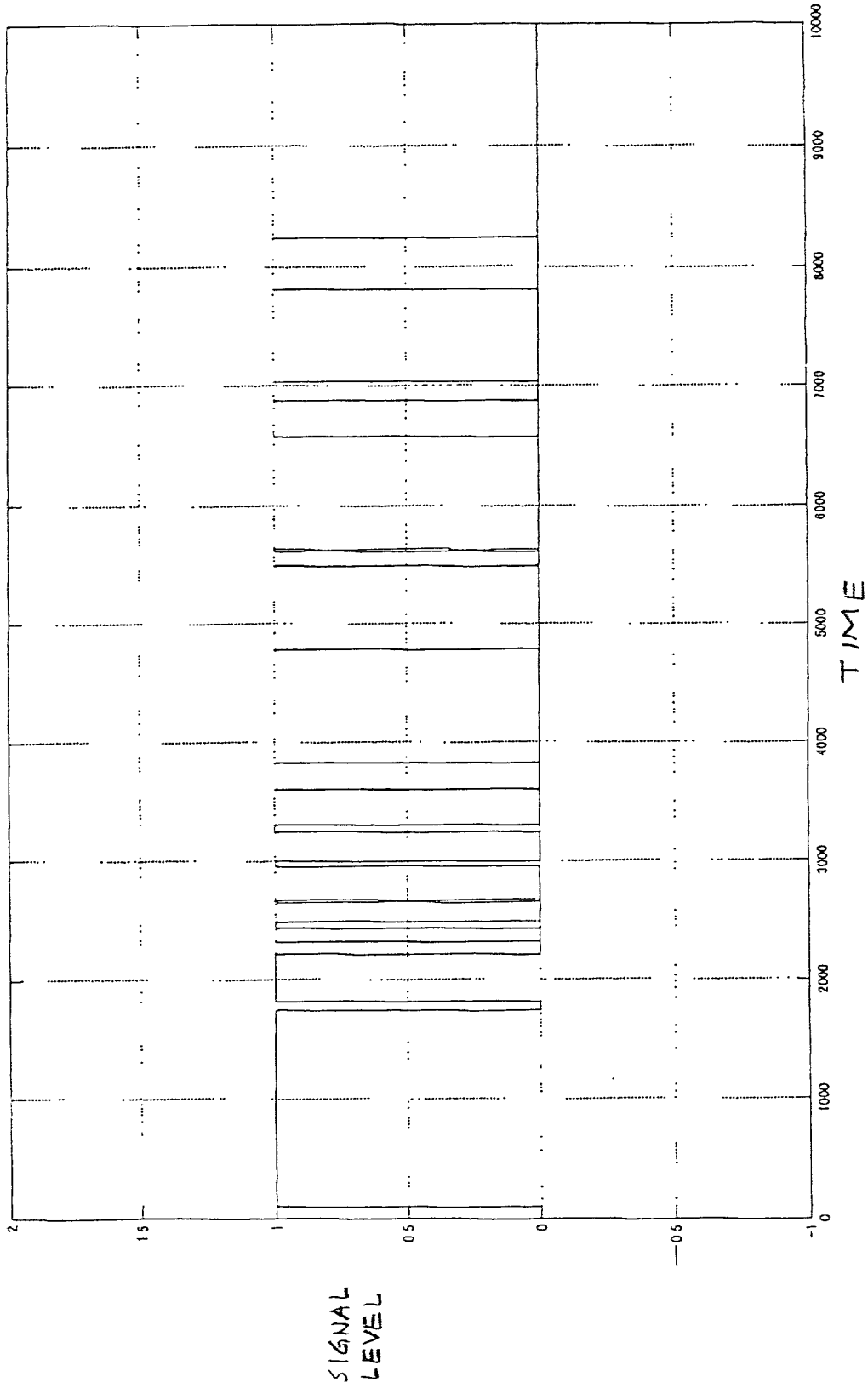
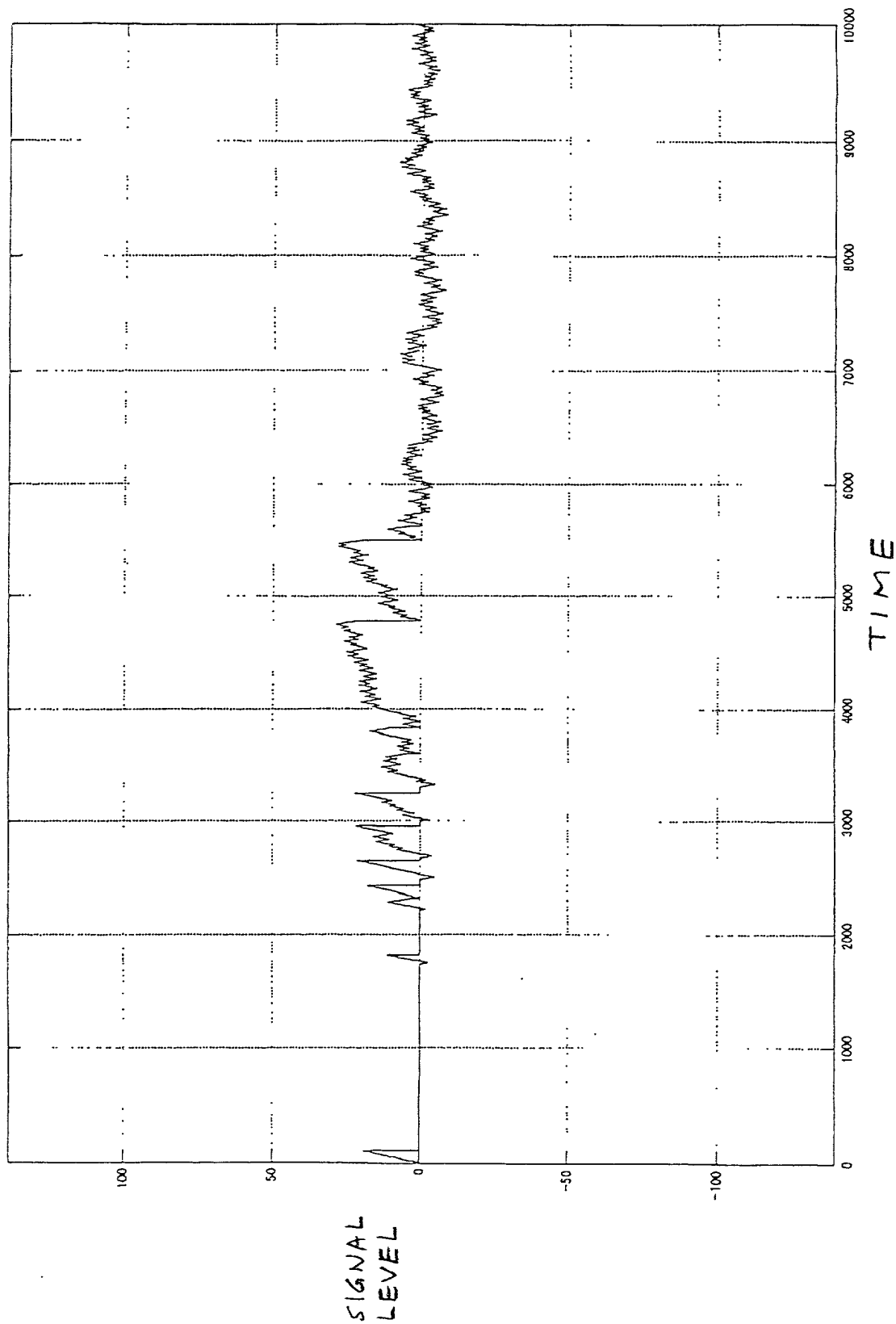


FIG. 59



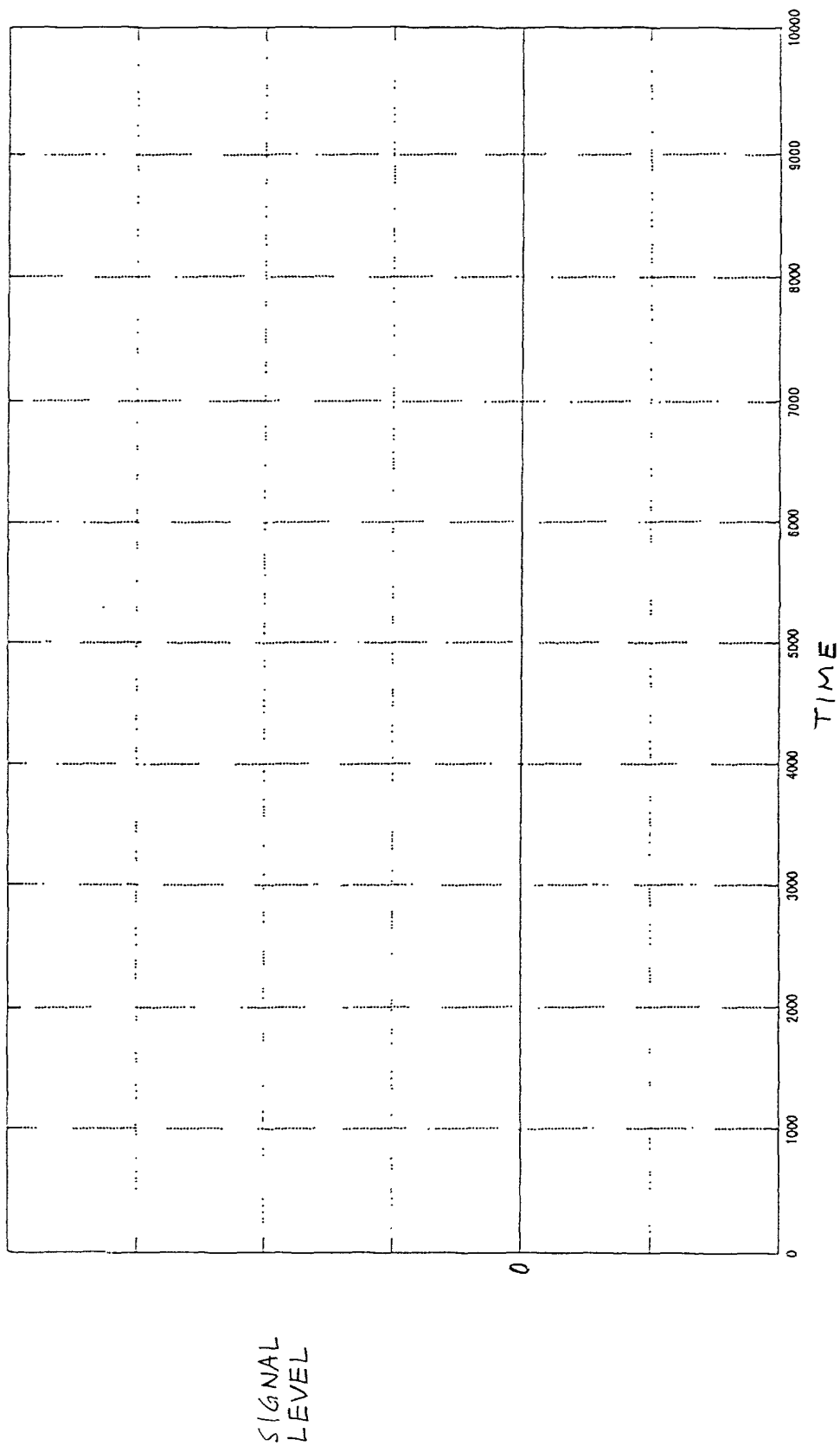
20220"454E300T

FIG. 60



20,220" HSE300T

FIG. 61



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